

FROM LTL TO rLTL MONITORING

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Joint Work With Corto Mascle, Daniel Neider, Paulo Tabuada,
Alexander Weinert, Martin Zimmermann

WHY rLTL RATHER THAN LTL?

Assumption \implies Guarantee

View Always Unobstructed \implies Always Stay on Lane

$G(\text{unobs. view}) \implies G(\text{on lane})$

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Problem 1: One Frame Camera Glitch \implies Do Whatever You Want

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Problem 1: One Frame Camera Glitch \implies Do Whatever You Want

Problem 2: Crash Immediately \iff Drive Perfectly

WHY rLTL RATHER THAN LTL?

**LTL to rLTL:
More Robustness
More Information**

View Always

Assumption \implies

Guarantee

Unobstructed \implies

Always Stay

on Lane

$G(\text{unobs.view}) \implies$

$G(\text{on lane})$

Problem 1: One Frame Camera Glitch \implies Do Whatever You Want

Problem 2: Crash Immediately \iff Drive Perfectly

Lift Monitoring from LTL to rLTL

**rLTL on
Finite Traces**



**Construction
of an rLTL
Monitor**



**Case Study:
LTL v. rLTL**

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What you need to know about (r)LTL semantics

LTL

$a \in \Sigma, \quad AP = 2^\Sigma, \quad \text{trace } \pi \in AP^\omega$

Example

$\varphi \equiv a$

$\pi = \{a\} * * *$

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$$\varphi \equiv \mathbf{F} a$$

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Output: 1/0

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"Ga"

"FGa"

"GFa"

"Fa"

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Finite Semantics: Ternary Output

1 – Already Satisfied

0 – Already Falsified

? – Don't Know

Finite Semantics: Ternary Output

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| Formula | Prefix | LTL | rLTL (G , FG , GF , F) |
|------------|---------------|-----|---|
| G a | ε | ? | ???? |
| | {a} | ? | ???1 |
| | {a}{ } | 0 | 0???1 |

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Questions: What truth values might occur?

Finite Semantics: Realizable Verdicts

| <i>Value</i> | <i>Prefix</i> | <i>Formula</i> | <i>Value</i> | <i>Prefix</i> | <i>Formula</i> |
|--------------|------------------|---|--------------|------------------|--|
| 0000 | ε | $a \wedge \neg a$ | 0?11 | $\emptyset\{a\}$ | $\Box a \vee \Box \neg a$ |
| 000? | ε | $\Diamond \Box a \wedge \Diamond \neg \Diamond a$ | 0111 | $\emptyset\{a\}$ | $a \mathbf{R} a$ |
| 0001 | unrealizable | | ???? | ε | $\Box a$ |
| 00?? | ε | $\Box a \wedge \Box \neg a$ | ???1 | $\{a\}$ | $\Box a$ |
| 00?1 | $\emptyset\{a\}$ | $\Box a \wedge \Box \neg a$ | ??11 | ε | $\Box a \vee \Diamond \neg \Diamond a$ |
| 0011 | unrealizable | | ?111 | ε | $\Box a \vee \neg \Diamond \neg \Diamond \neg a$ |
| 0??? | \emptyset | $\Box a$ | 1111 | ε | $a \vee \neg a$ |
| 0??1 | $\emptyset\{a\}$ | $\Box a$ | | | |

Finite Semantics: Realizable Verdicts

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| 00?? | ε | $\Box a \wedge \Box \neg a$ | ????1 | $\{a\}$ | $\Box a$ |
| 00?1 | $\emptyset\{a\}$ | $\Box a \wedge \Box \neg a$ | ??11 | ε | $\Box a \vee \Diamond \neg \Diamond a$ |
| 0011 | unrealizable | | ?111 | ε | $\Box a \vee \neg \Diamond \neg \Diamond \neg a$ |
| 0??? | \emptyset | $\Box a$ | 1111 | ε | $a \vee \neg a$ |
| 0??1 | $\emptyset\{a\}$ | $\Box a$ | | | |

Theorem: An rLTL Monitor cannot yield 0001 nor 0011.

Finite Semantics: Ternary Output

1 – Already Satisfied

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? – Undetermined

| Formula | Prefix | LTL | rLTL (G, FG, GF, F) |
|-----------|------------|-----|---------------------------------|
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Questions: How do values "evolve"?

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Questions: How do values "evolve"?

Theorem: Up to four refinements are possible.

Monitorability

rLTL-Ugly Prefix: Every continuation yields **????**

rLTL-Monitorable: There are no **rLTL-Ugly Prefixes**

| | LTL Monitorable | Not LTL Monitorable |
|----------------------|----------------------------------|--|
| rLTL Monitorable | Ga | GFa |
| Not rLTL Monitorable | (Ga ∧ G¬a) ⇒ (FGa ∧ FG¬a) | (p ∧ φ_{LTL}) ∨ (¬p ∧ φ_{rLTL}) |

Monitorability

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| rLTL Monitorable | rLTL: "Adding { } will always yield 0****" \mathbf{Ga} LTL: "Adding { } will always yield 0" | \mathbf{GFa} |
| Not rLTL Monitorable | $(\mathbf{Ga} \wedge \mathbf{G}\neg a) \implies (\mathbf{FGa} \wedge \mathbf{FG}\neg a)$ | $(p \wedge \varphi_{\text{LTL}}) \vee (\neg p \wedge \varphi_{\text{rLTL}})$ |

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LTL-MON DOES NOT IMPLY rLTL-MON

rLTL-Ugly Prefix: Every continuation yields ????

rLTL-Monitorable: There are no rLTL-Ugly Prefixes

$$(G a \wedge G \neg a) \implies (F G a \wedge F \underline{G} \neg a)$$

LTL-mon

$(G a \wedge G \neg a)$: Contradiction

$(G a \wedge G \neg a) \implies (F G a \wedge F \underline{G} \neg a)$: Tautology

Ugly Prefix { }a}

$\forall \rho$: { }a}ρ{ }^ω yields 1111

{ }a}ρ{a}^ω yields 0000

Not rLTL-mon

Monitorability

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Lift Monitoring from LTL to rLTL

**rLTL on
Finite Traces**

```
graph LR; A[rLTL on Finite Traces] --> B[Construction of an rLTL Monitor]; B --> C[Case Study: LTL v. rLTL]
```

**Construction
of an rLTL
Monitor**

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Constructing an rLTL Monitor

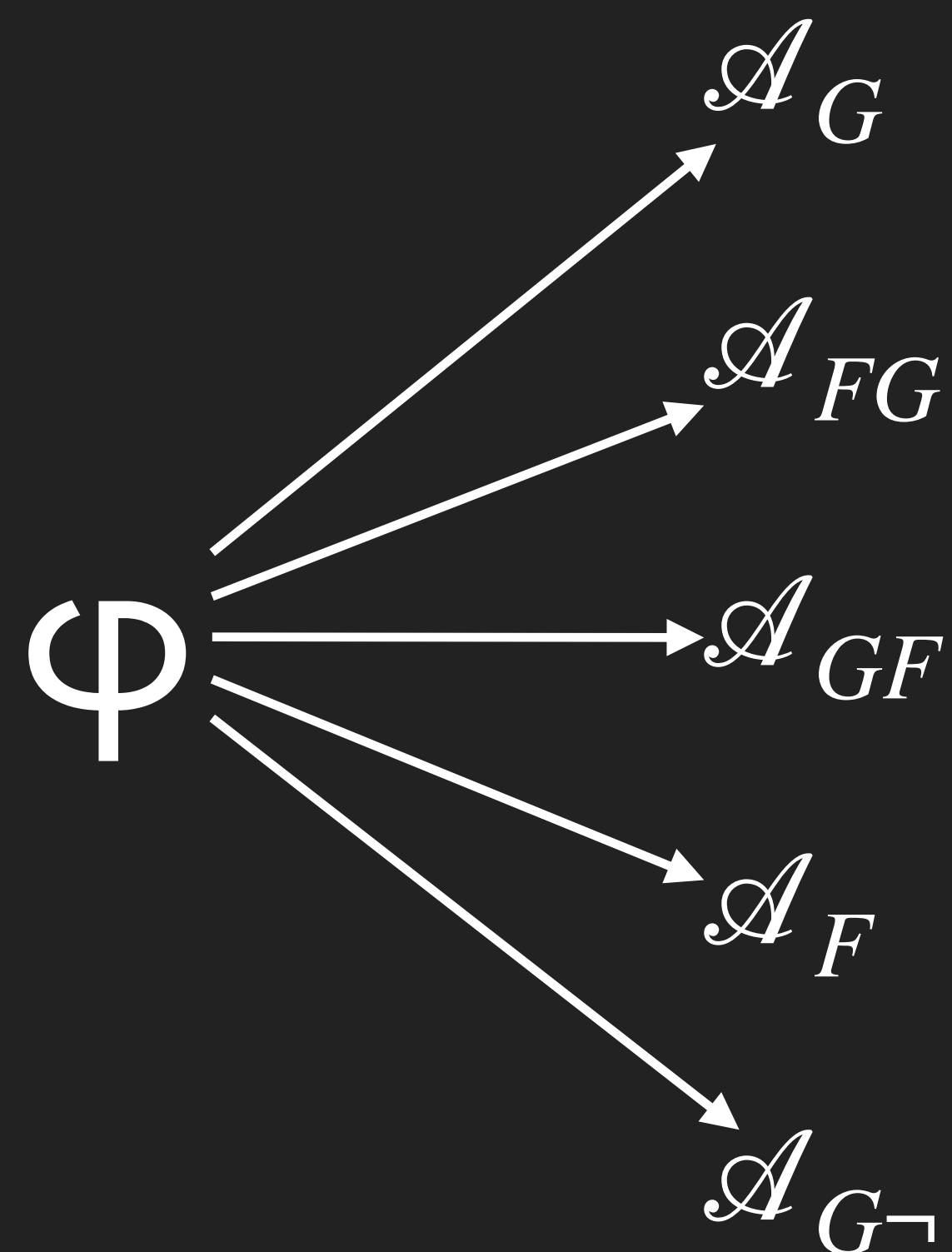
rLTL

Φ

Constructing an rLTL Monitor

rLTL

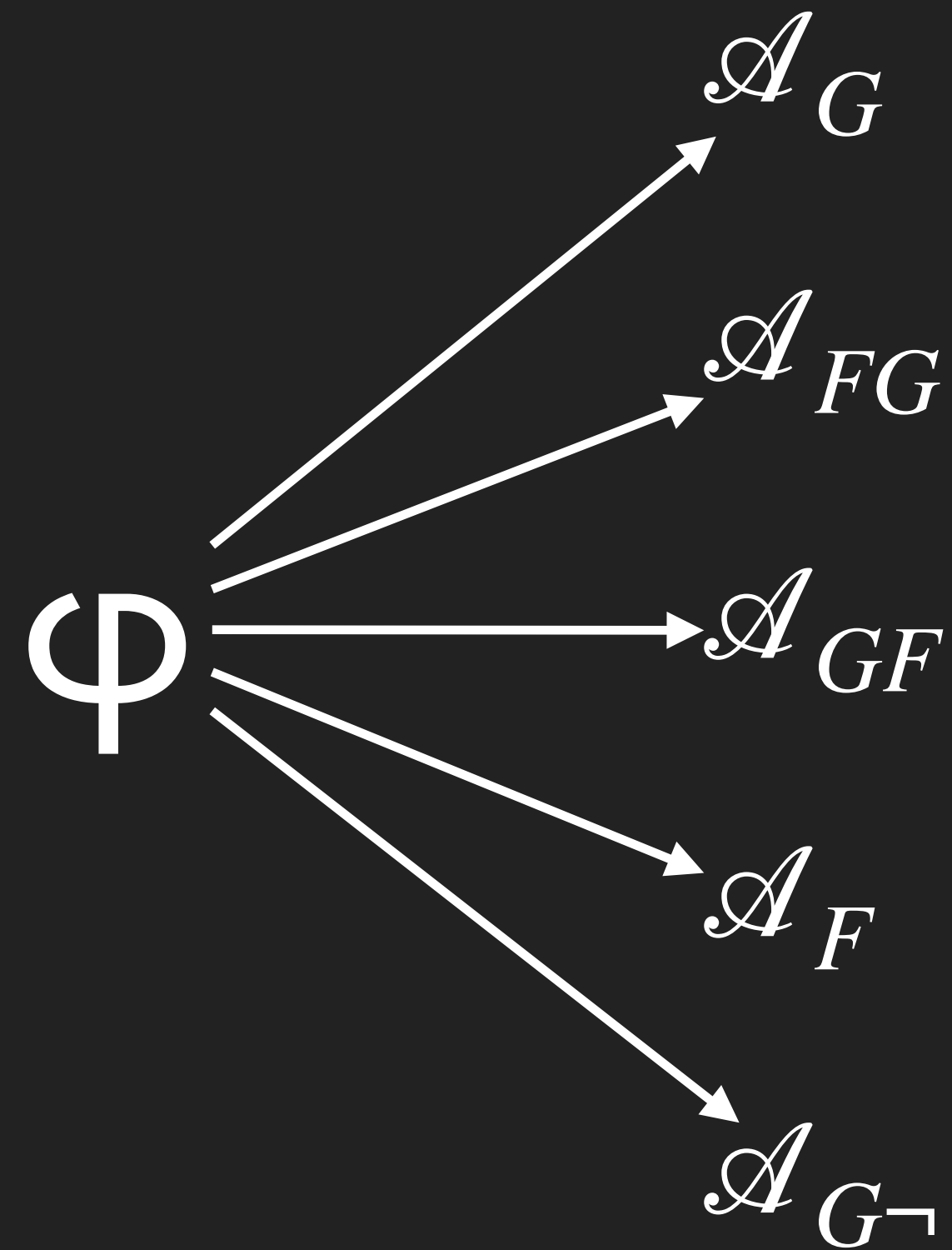
Büchi



Constructing an rLTL Monitor

rLTL

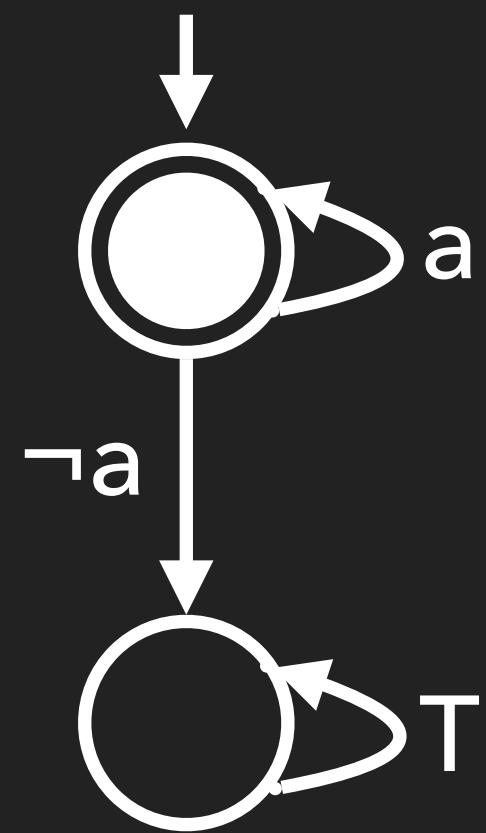
Büchi



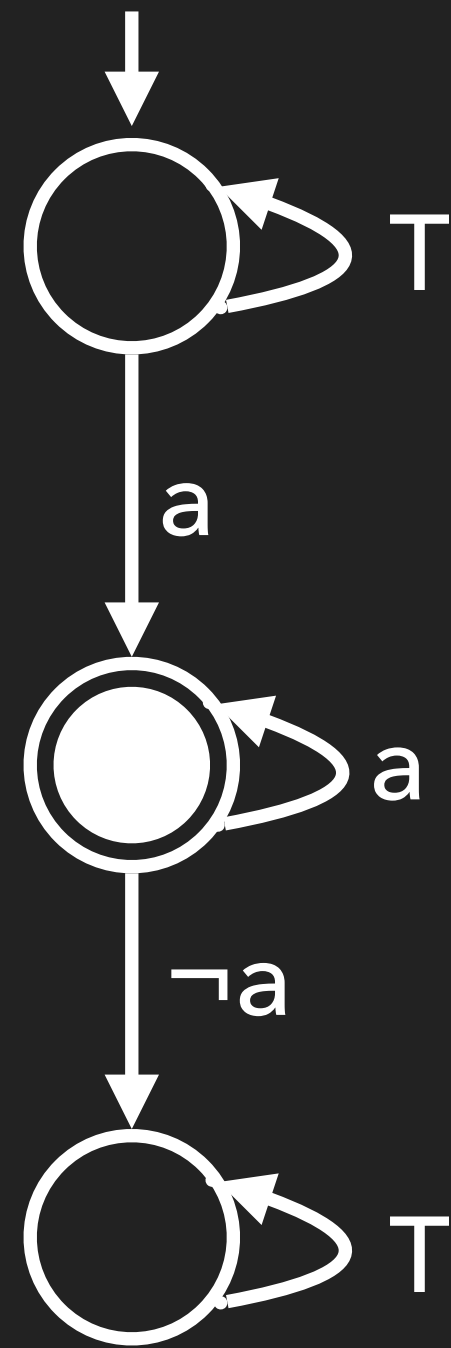
$$2^{O(|\phi|)}$$

Constructing an rLTL Monitor

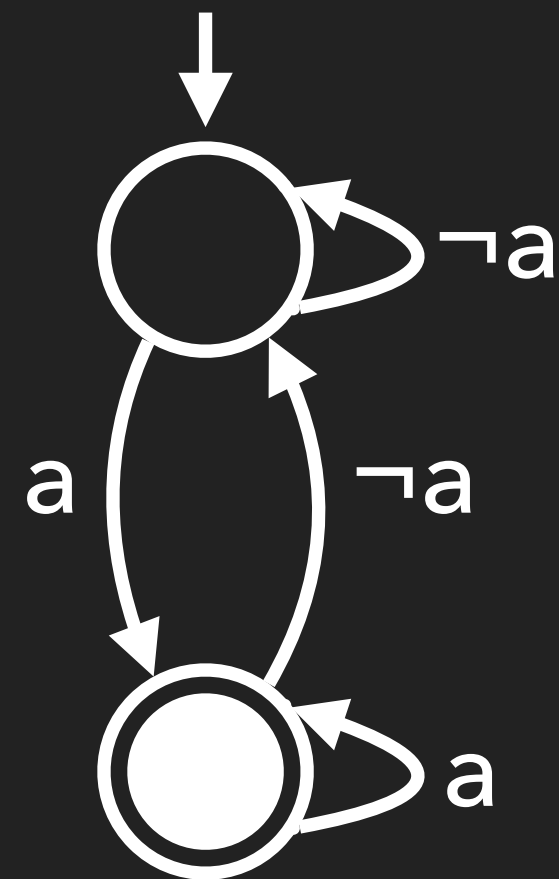
$G a$



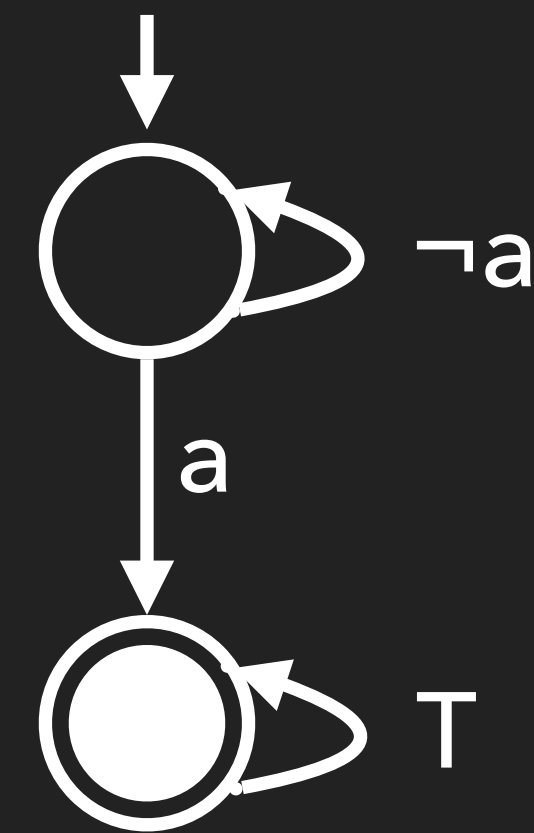
$FG a$



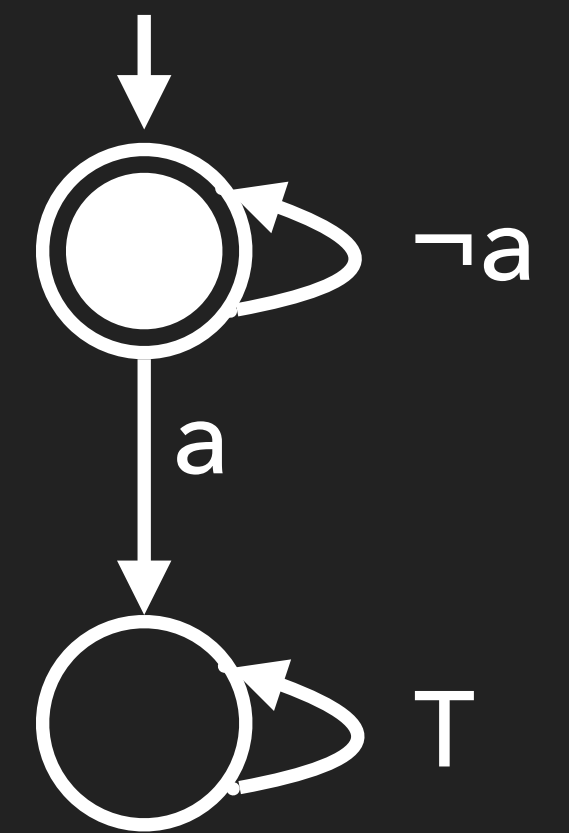
$GF a$



$F a$



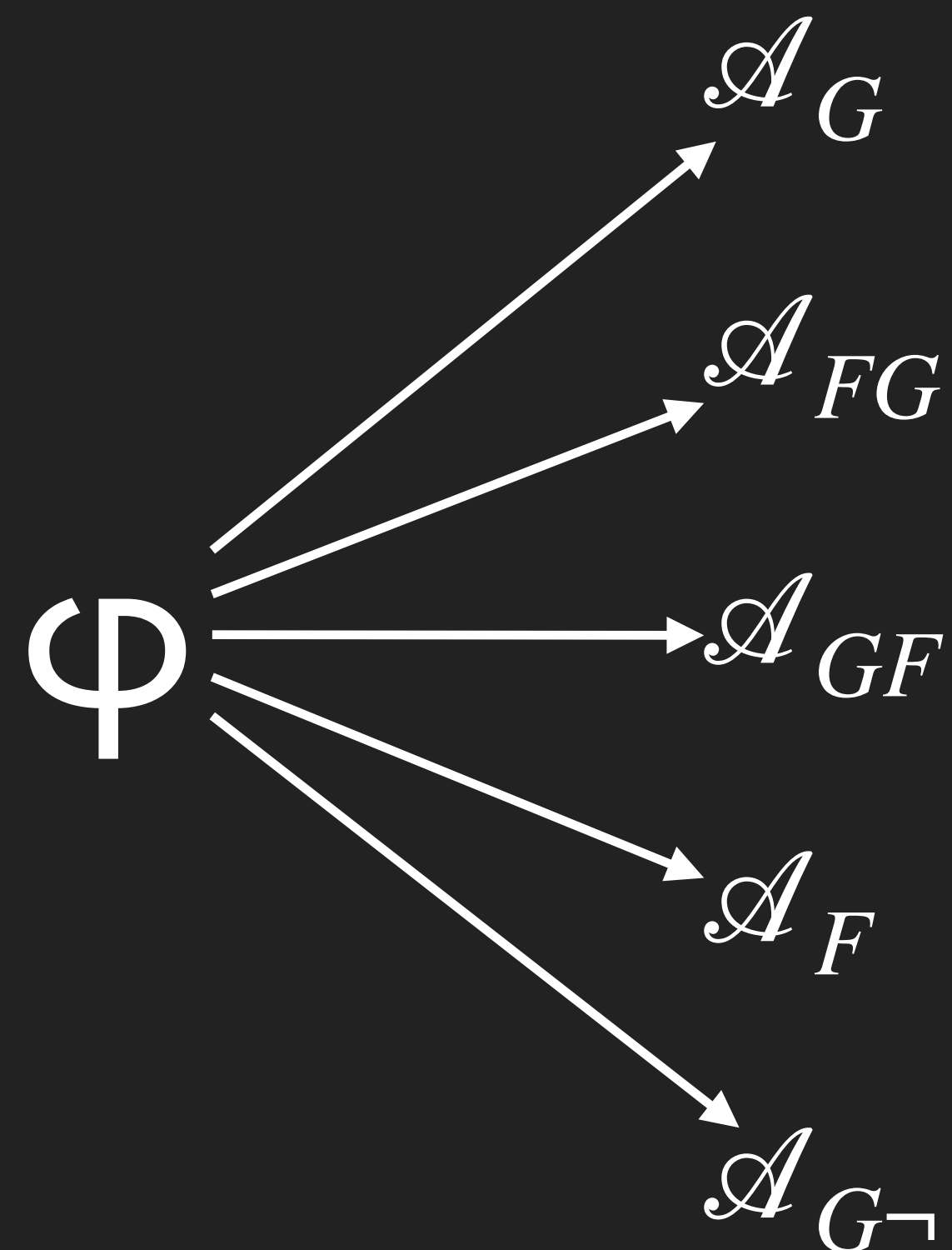
$G \neg a$



Constructing an rLTL Monitor

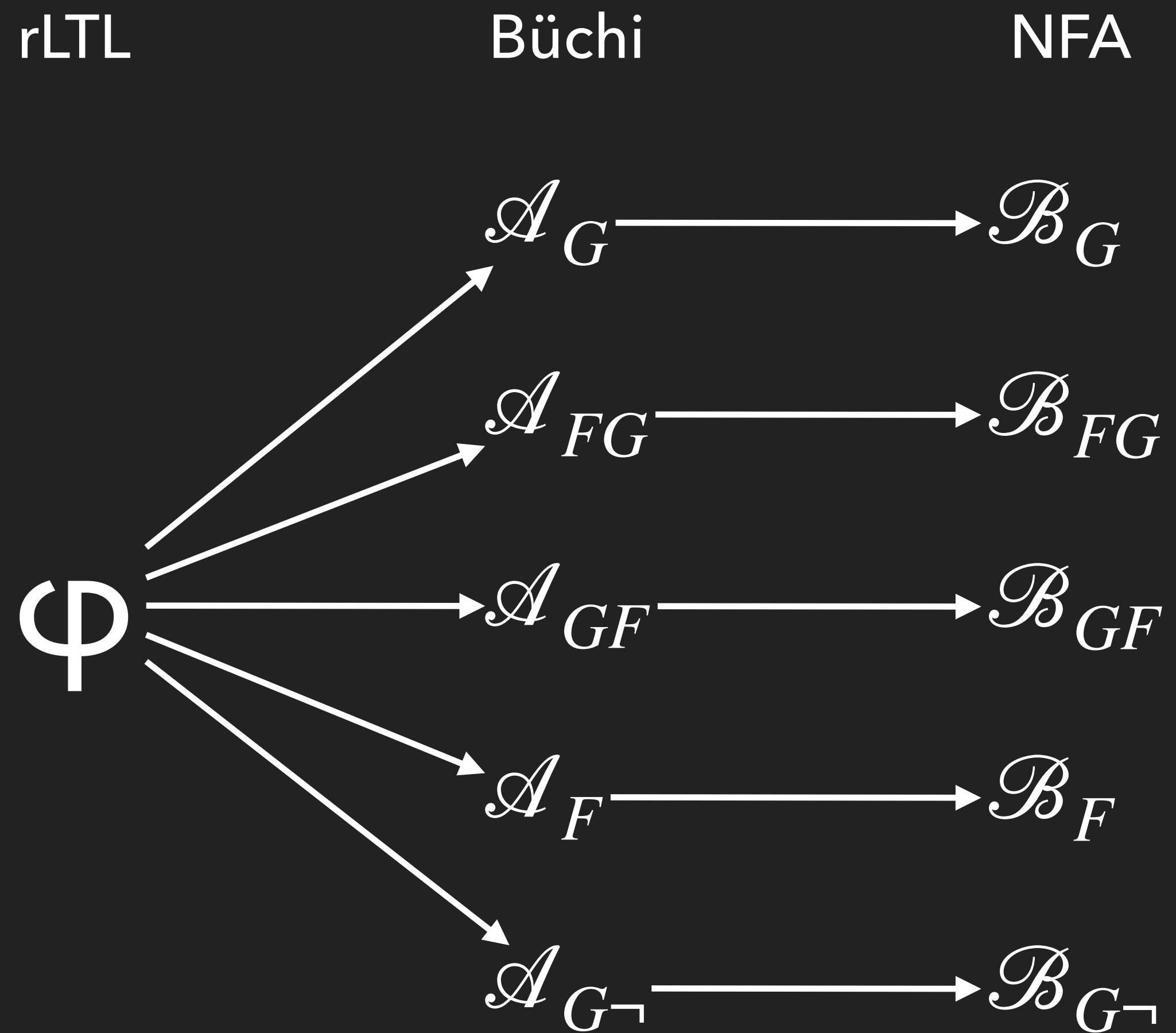
rLTL

Büchi



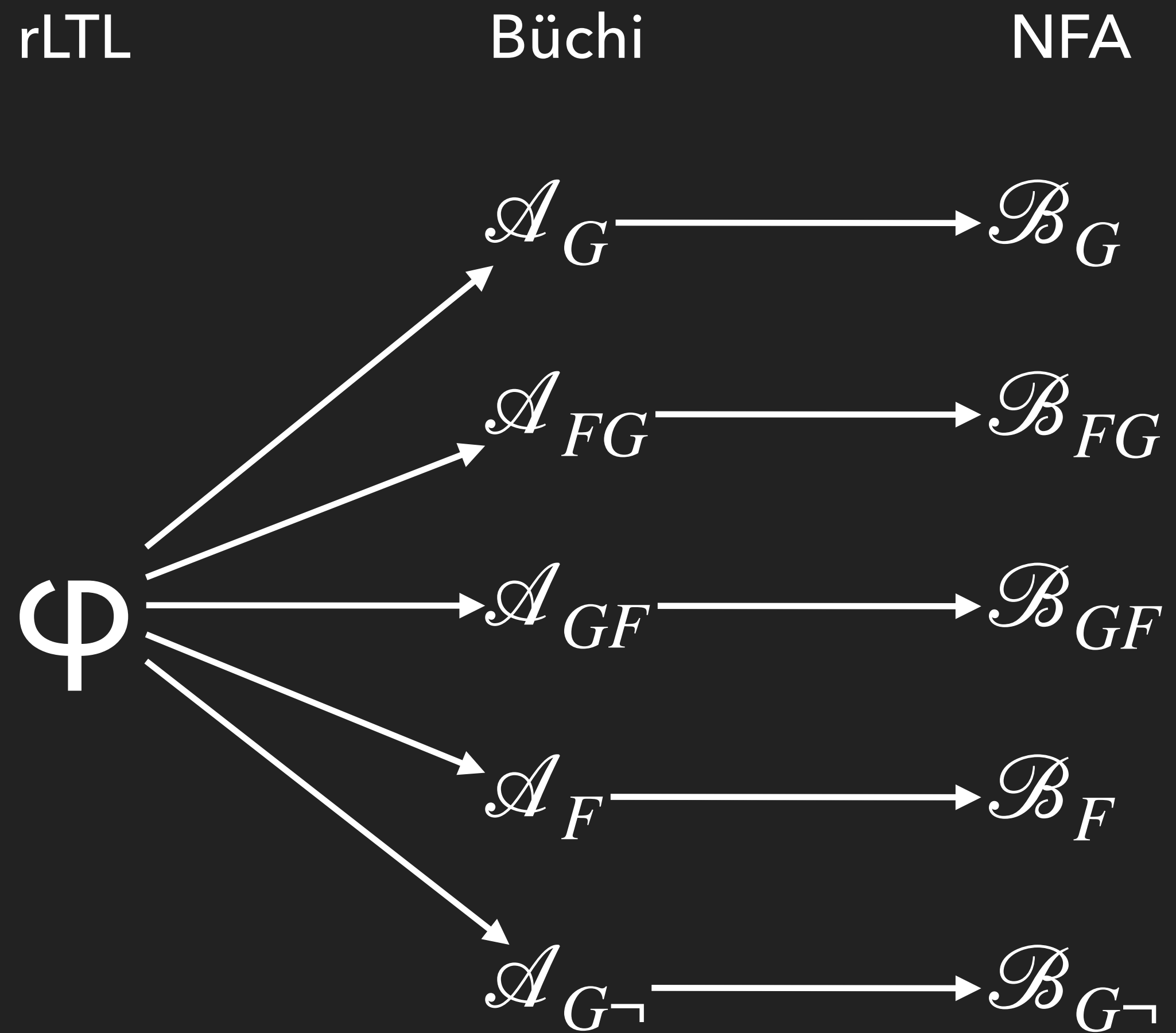
$$2^{O(|\varphi|)}$$

Constructing an rLTL Monitor



$$2^{O(|\varphi|)}$$

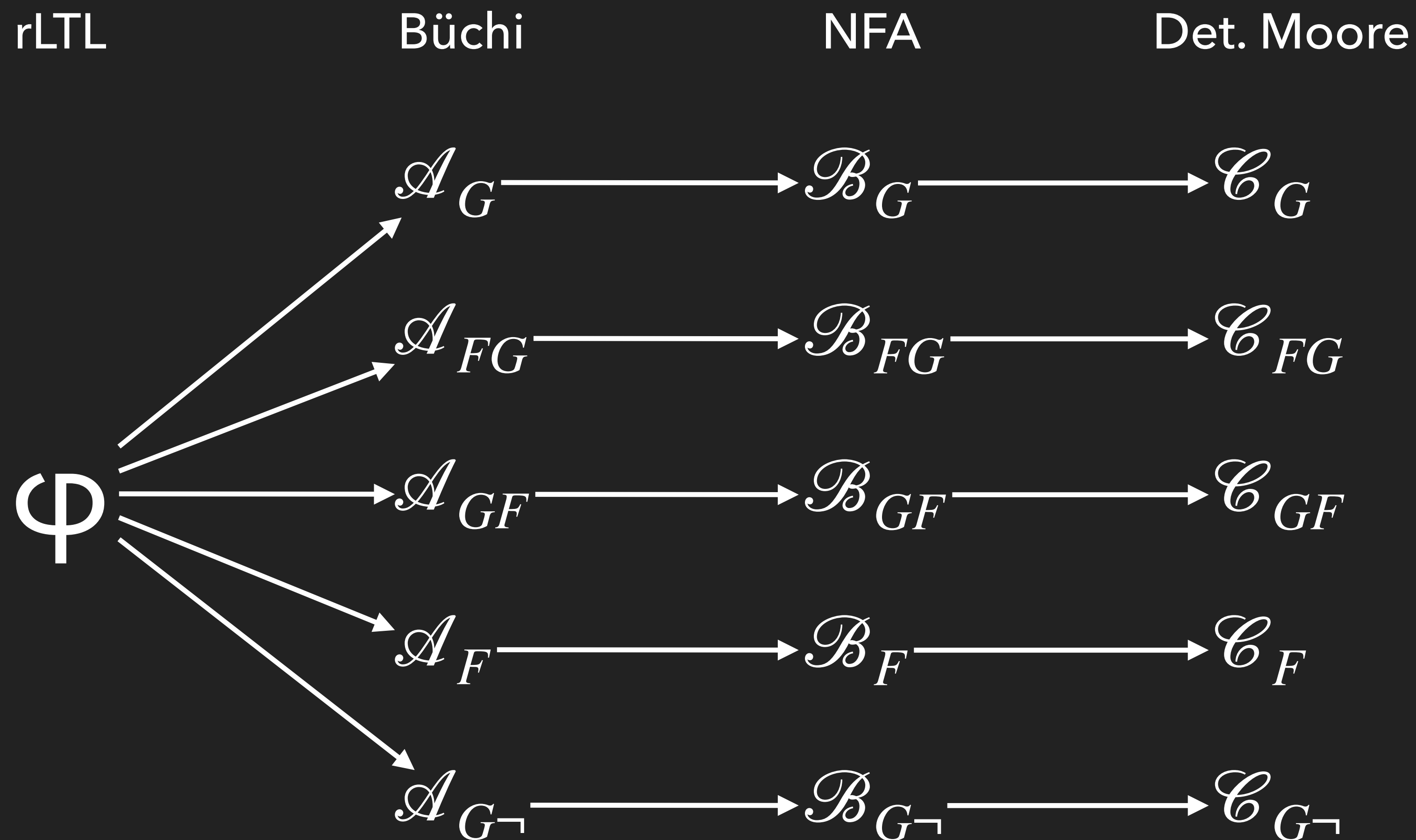
Constructing an rLTL Monitor



$$2^{O(|\varphi|)}$$

$$O(|\mathcal{A}|^3)$$

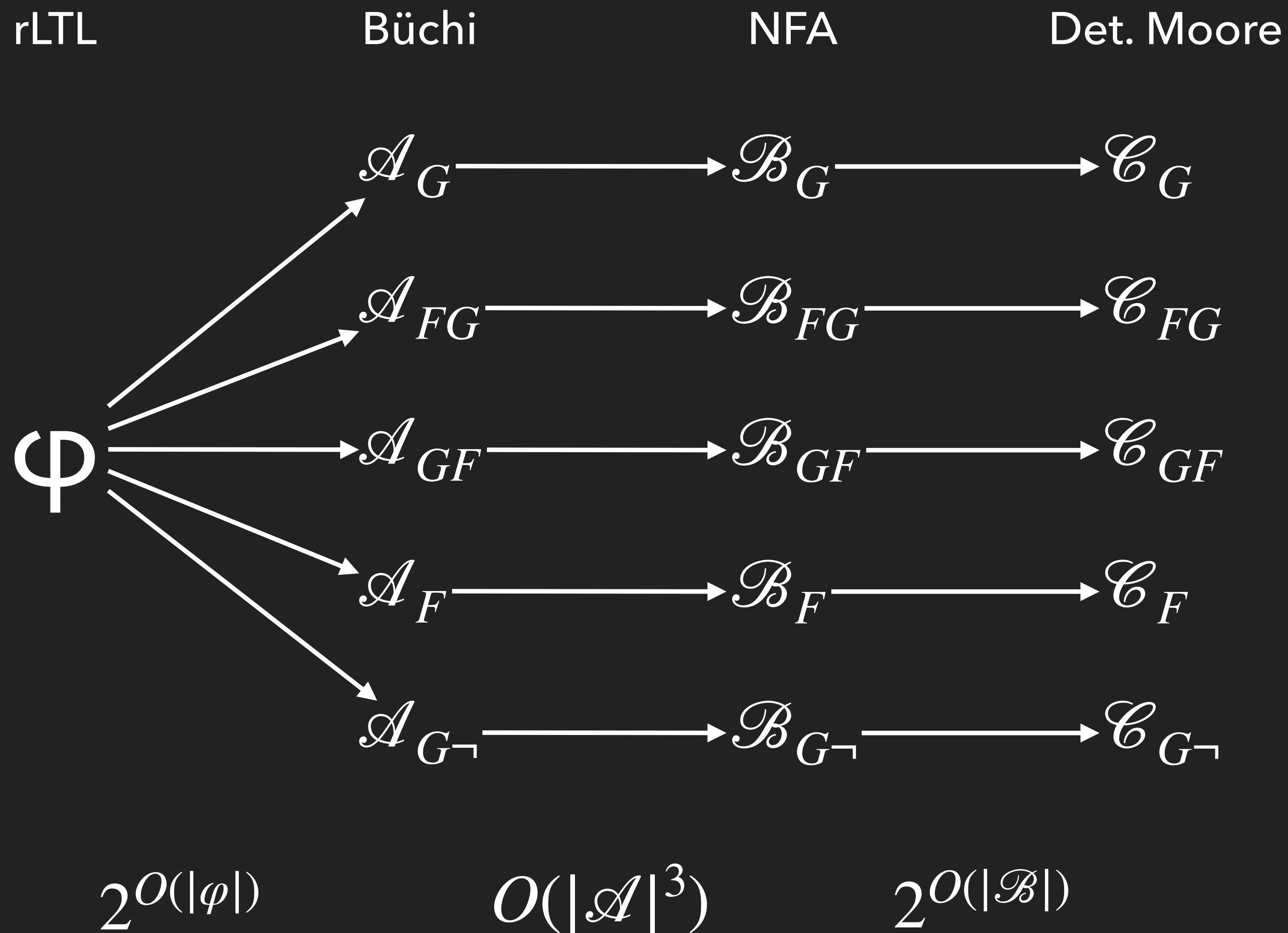
Constructing an rLTL Monitor



$$2^{O(|\varphi|)}$$

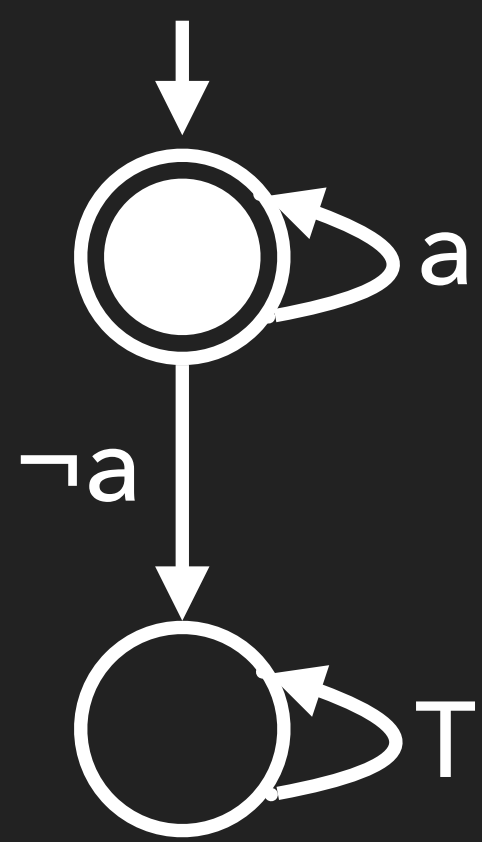
$$O(|\mathcal{A}|^3)$$

Constructing an rLTL Monitor

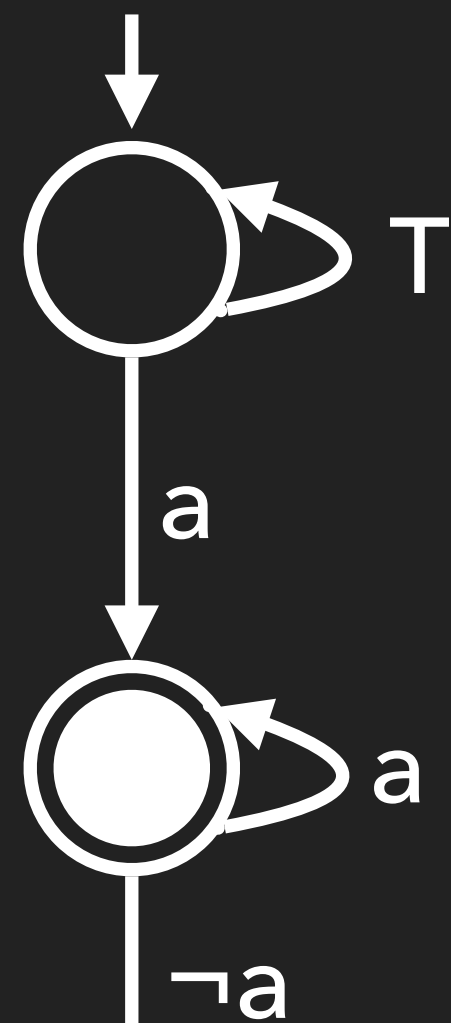


Constructing an rLTL Monitor

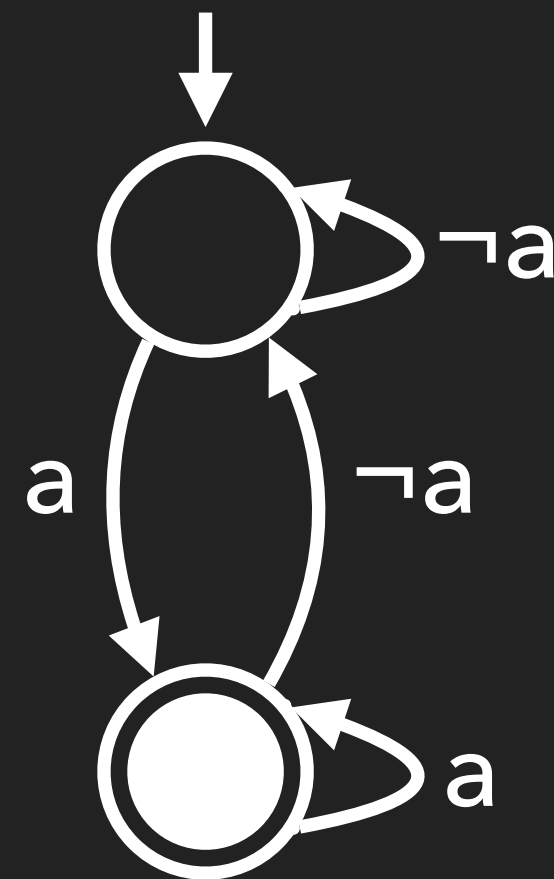
$G a$



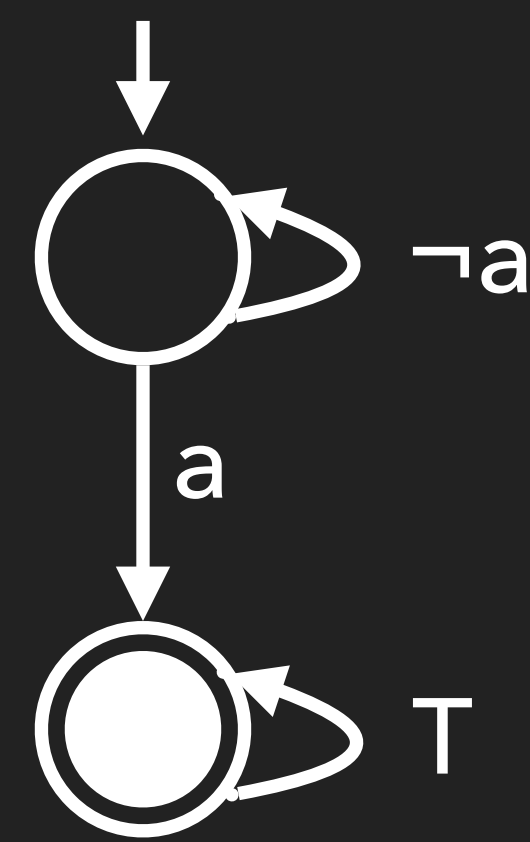
$FG a$



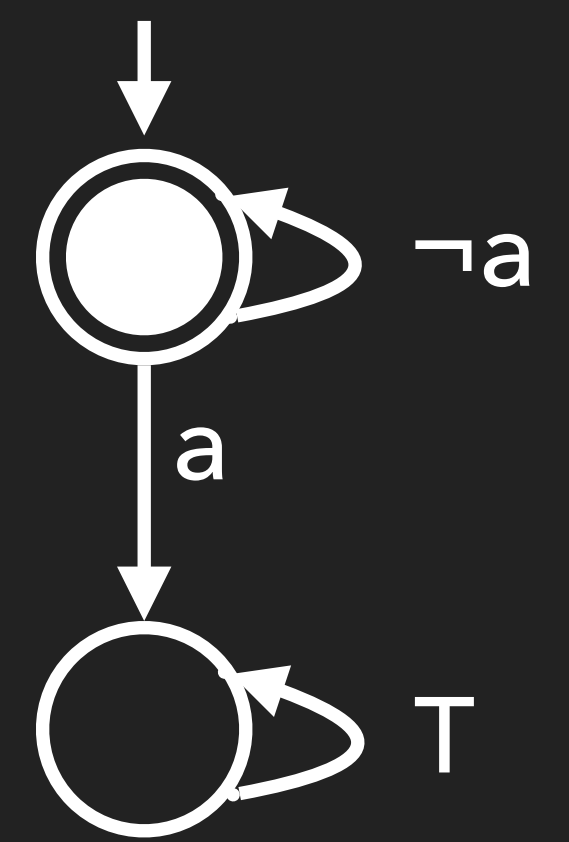
$GF a$



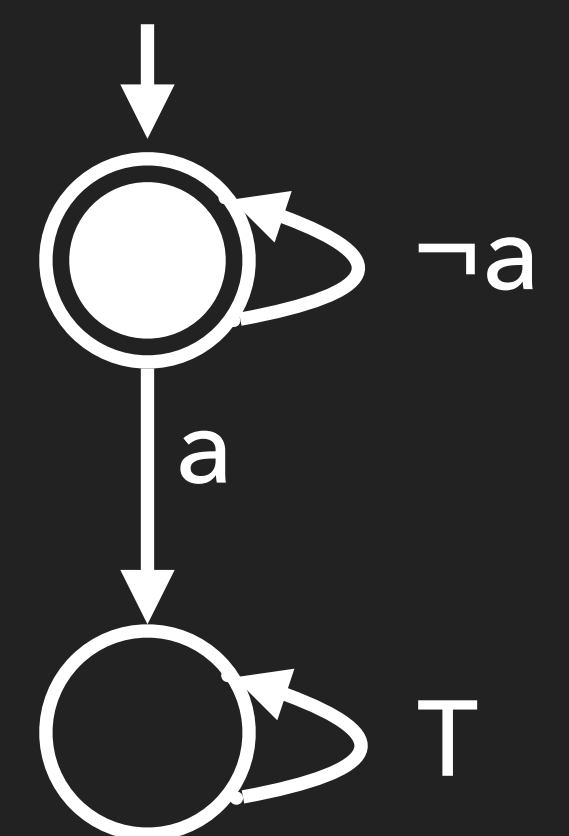
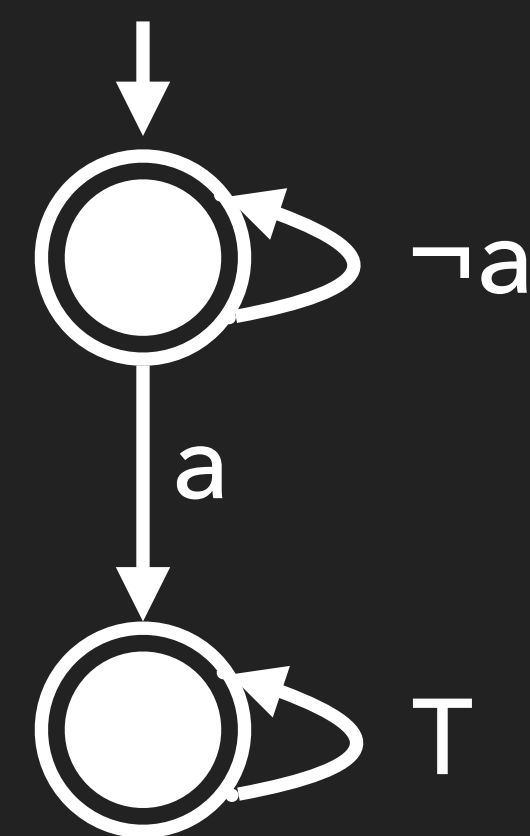
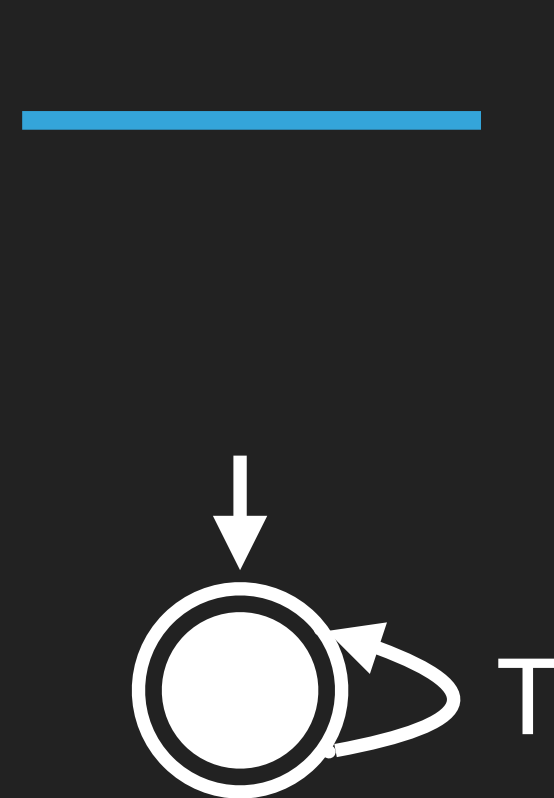
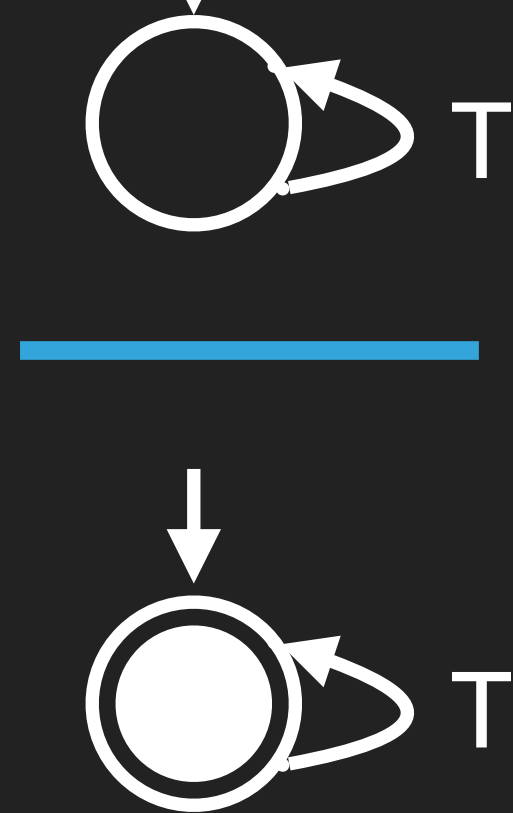
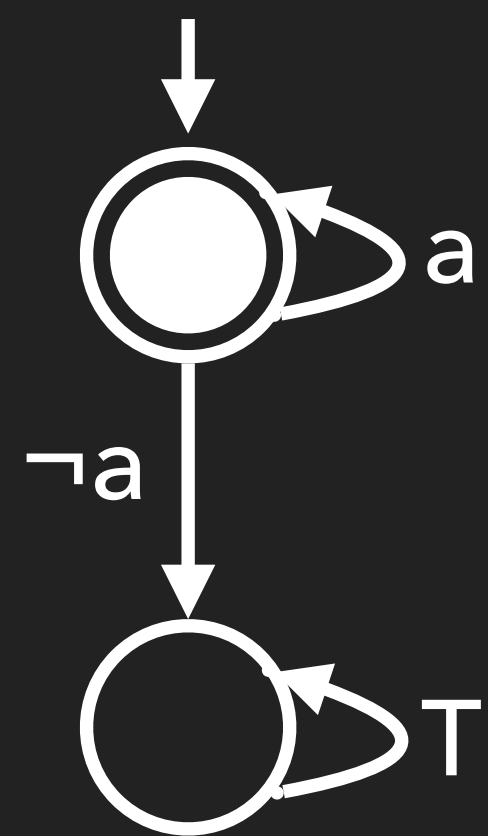
$F a$



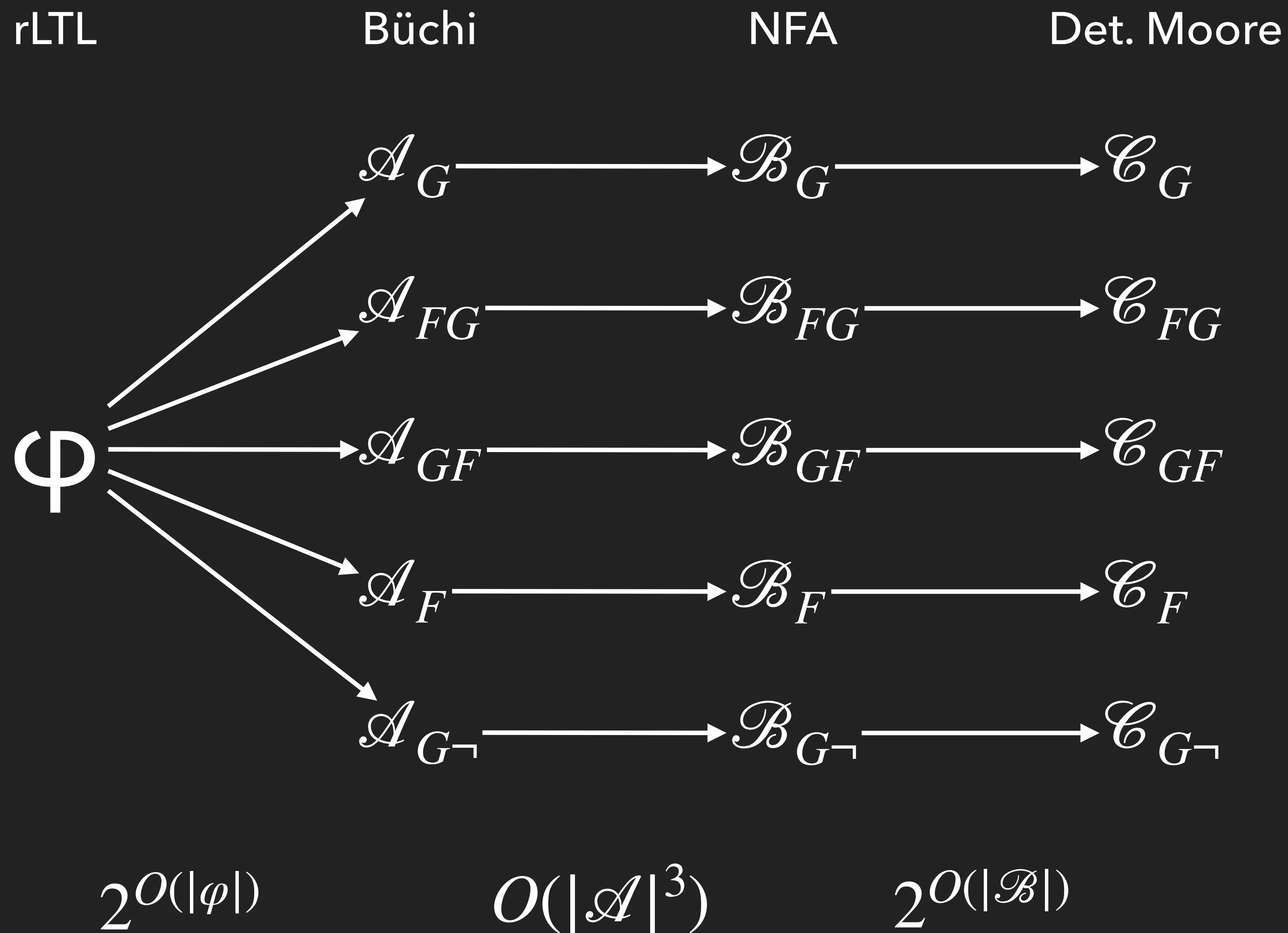
$G \neg a$



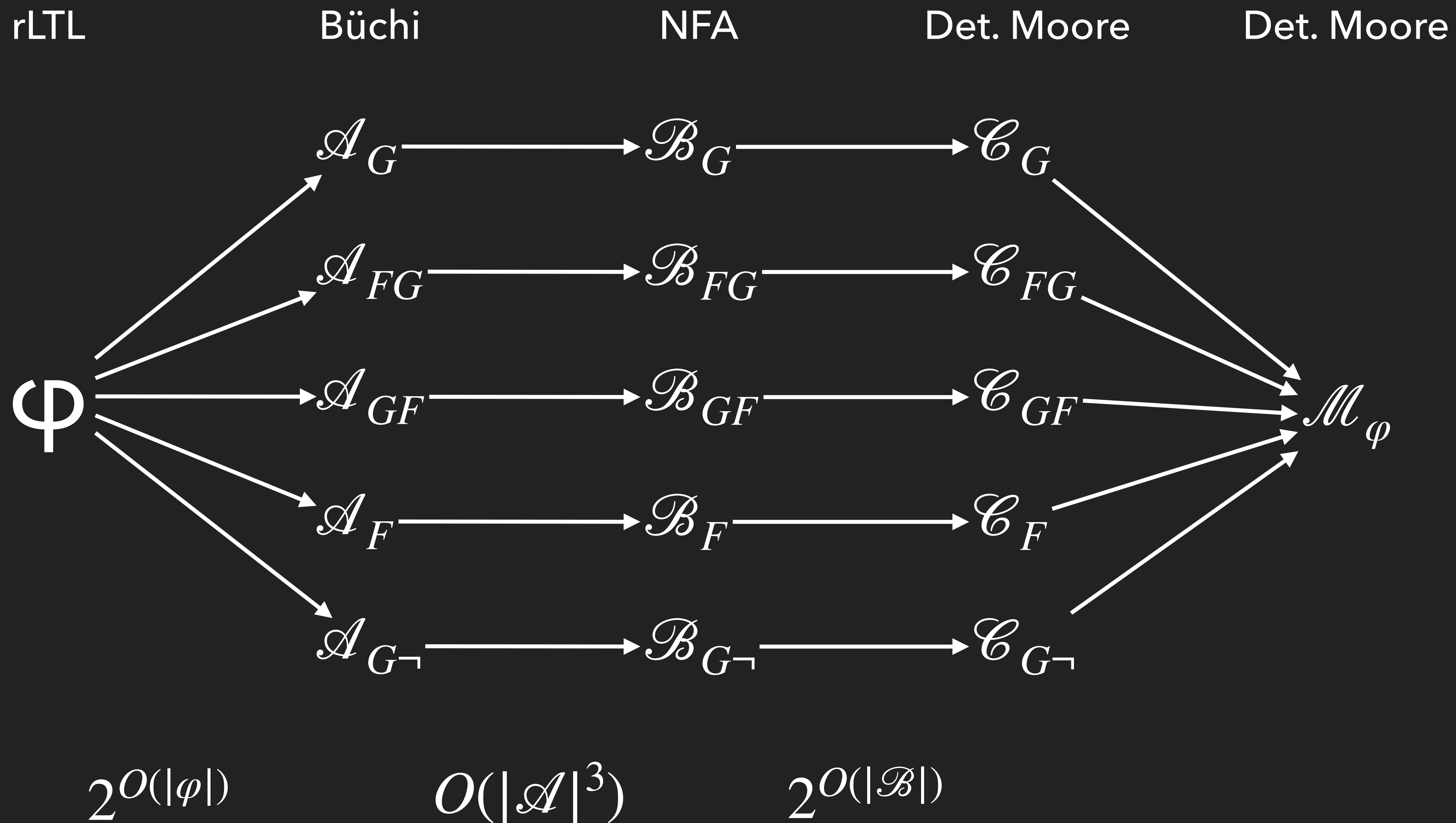
Büchi
DFA



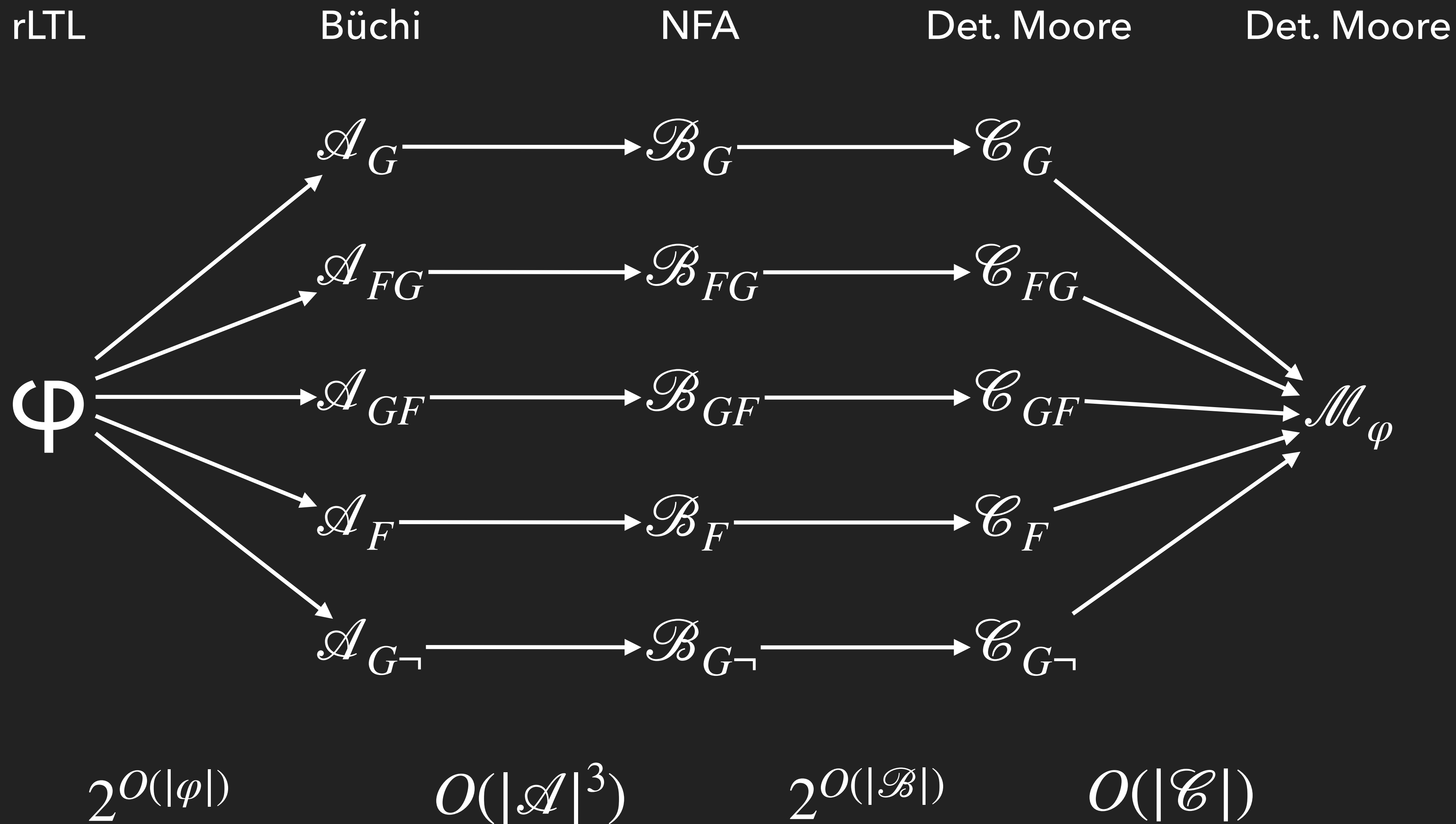
Constructing an rLTL Monitor



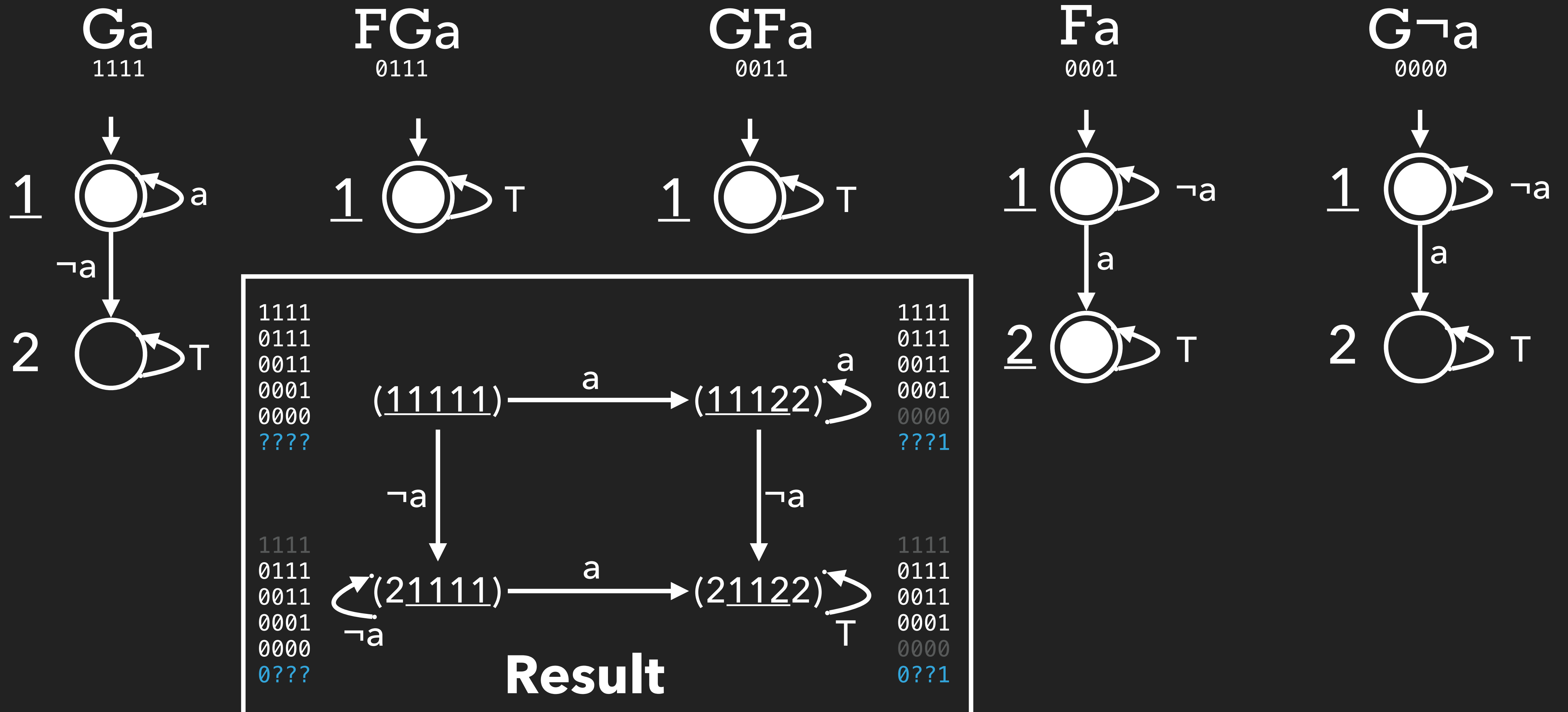
Constructing an rLTL Monitor



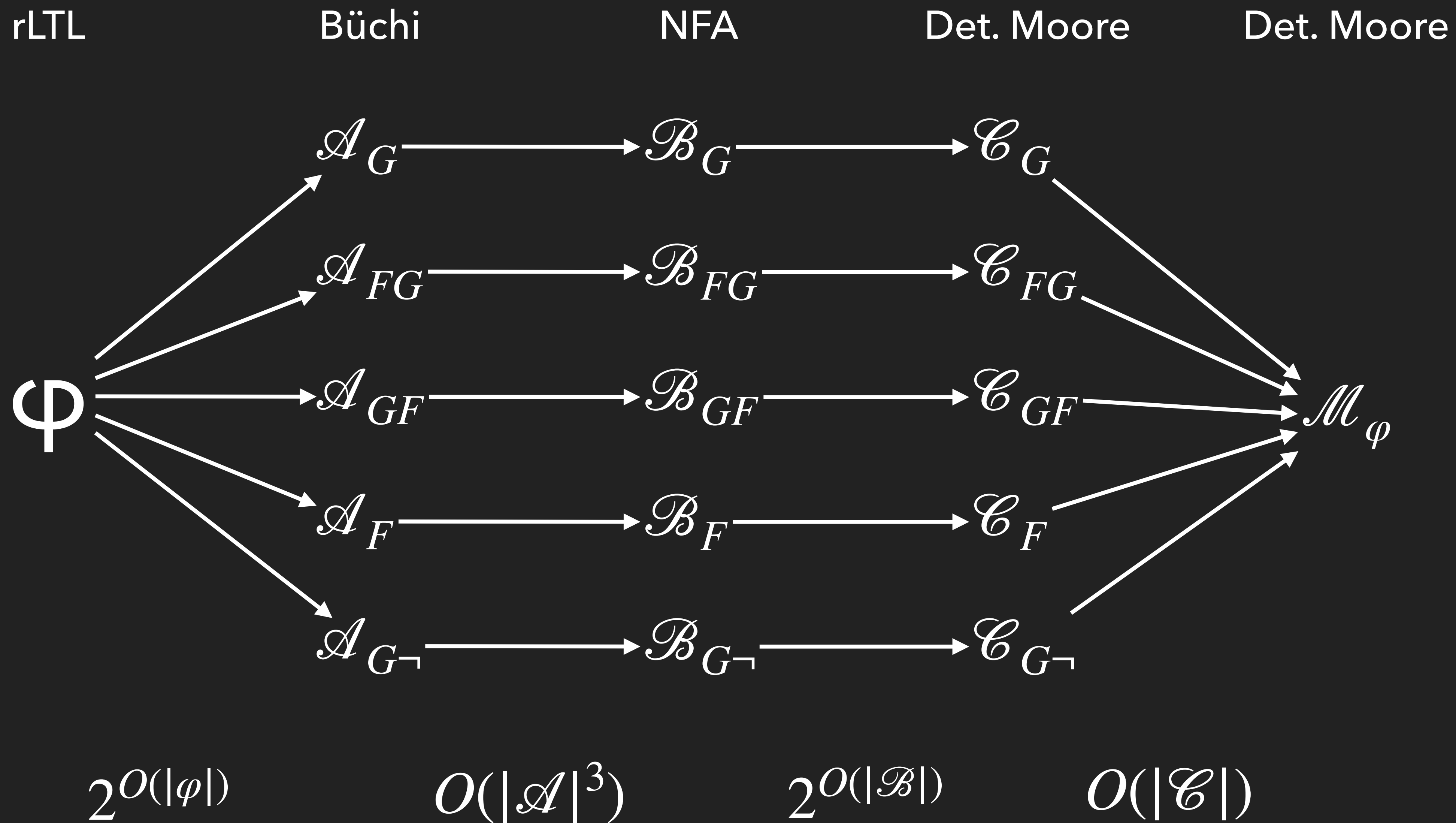
Constructing an rLTL Monitor



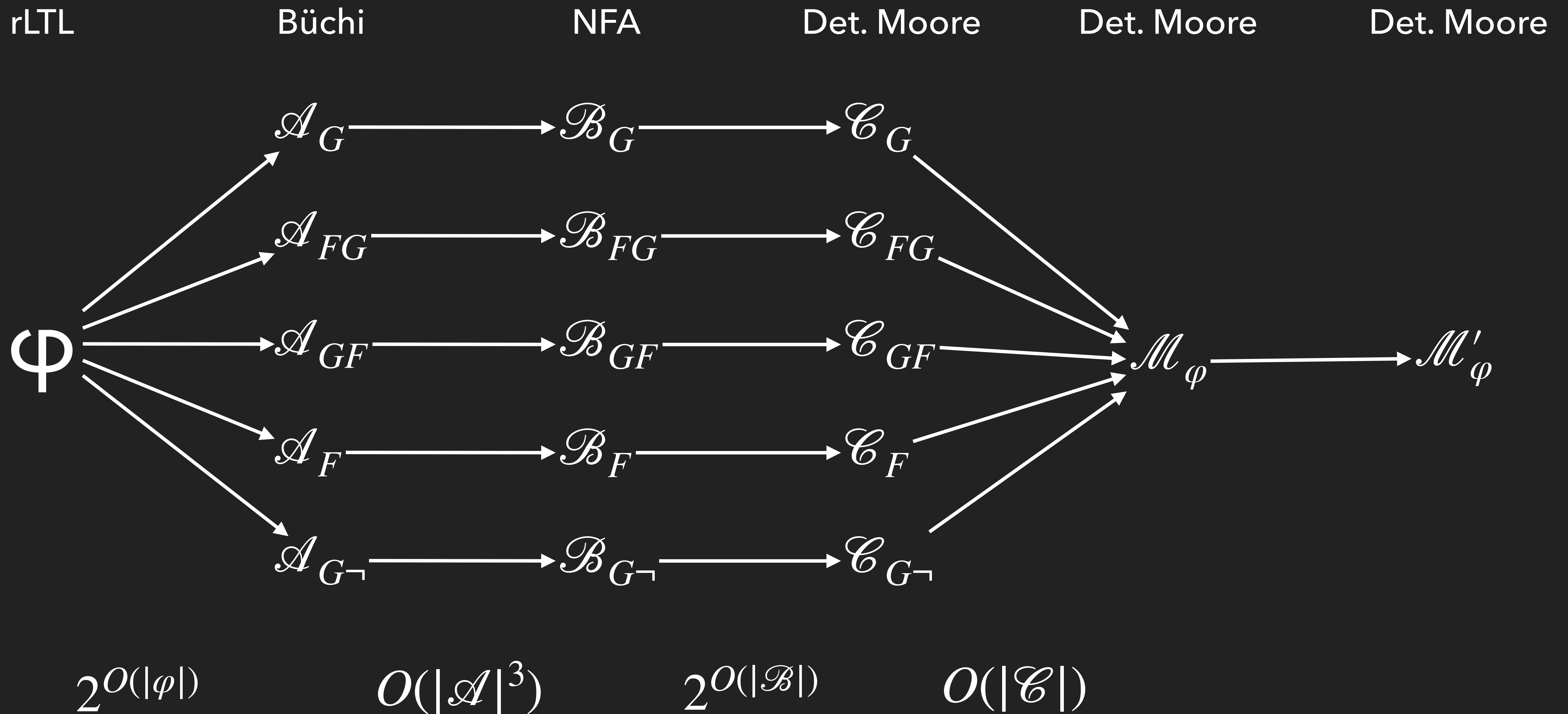
Constructing an rLTL Monitor



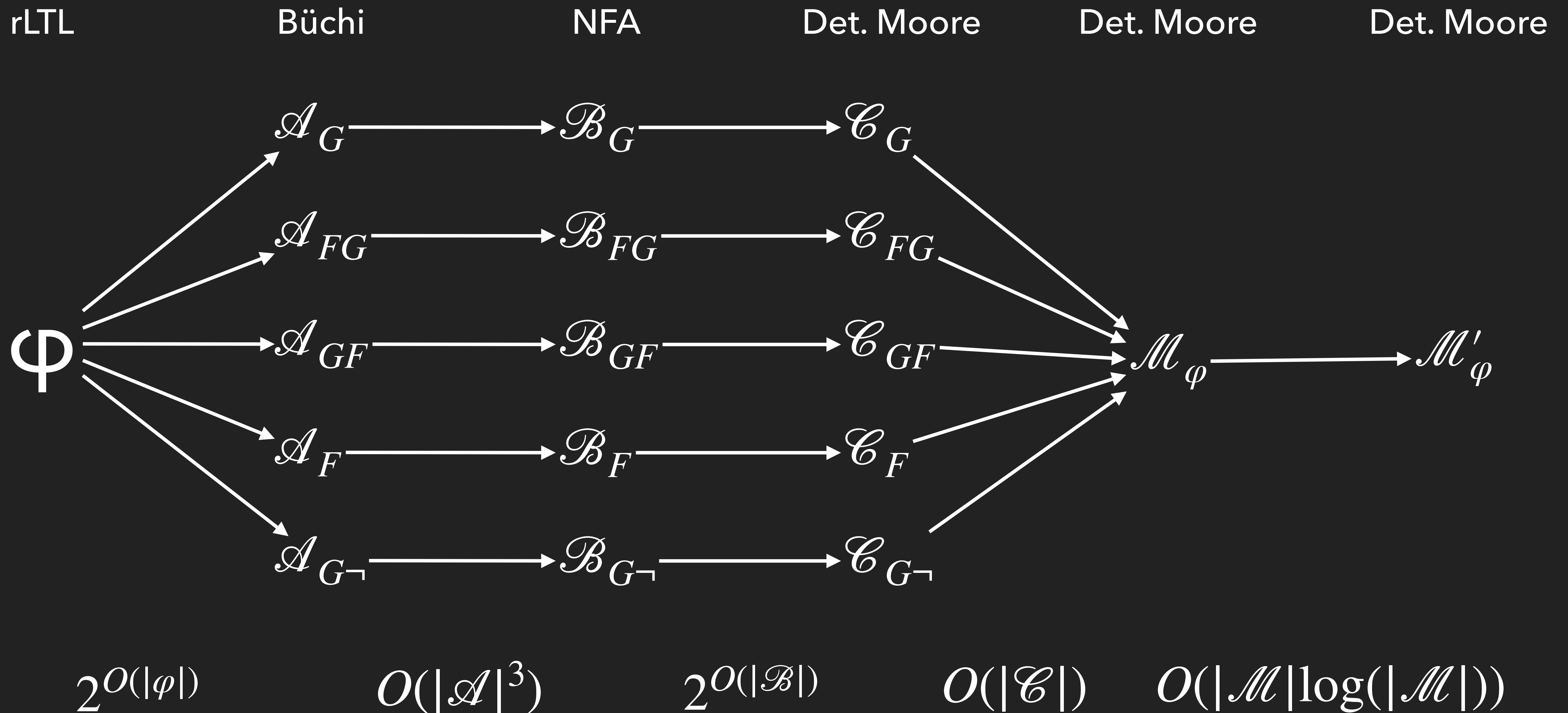
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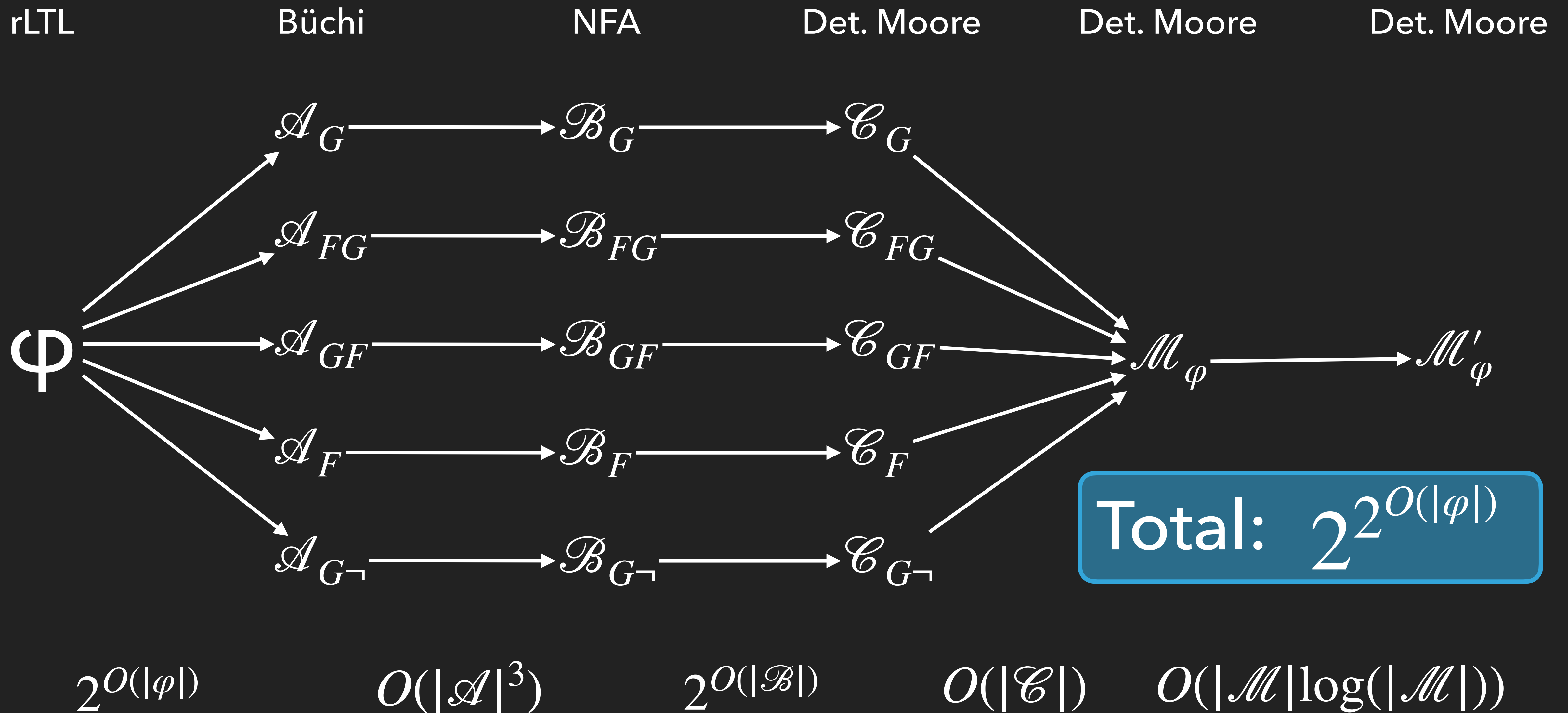
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Constructing an rLTL Monitor



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**rLTL on
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Benchmark

Dwyer et al [1]:

97 LTL formulas

frequent specification patterns

Benchmark

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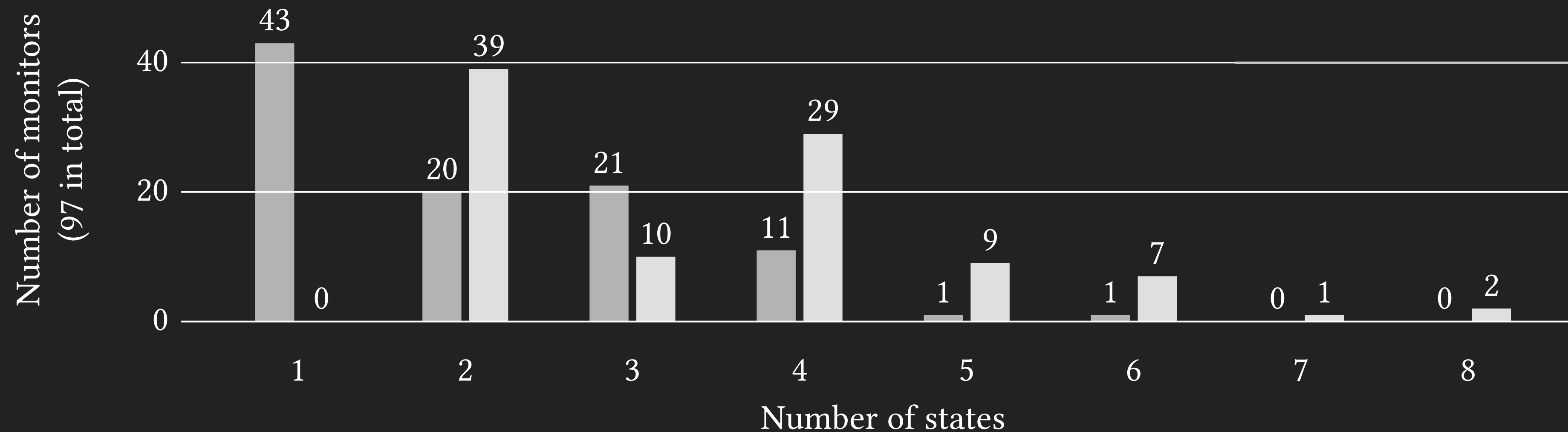
frequent specification patterns

55.7% LTL-monitorable [2] versus **100% rLTL**-monitorable

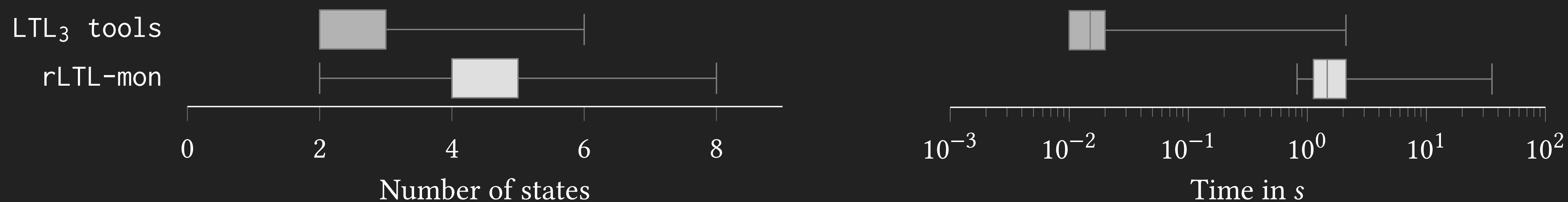
[1] Dwyer, Avrunin, Corbett. "Patterns in property specifications for finite-state verification". ICSE 1999

[2] Bauer, Leucker, Schallhart. "Runtime verification for LTL and TLTL". ACM Trans. Softw. Eng. Methodol. 2011

Results

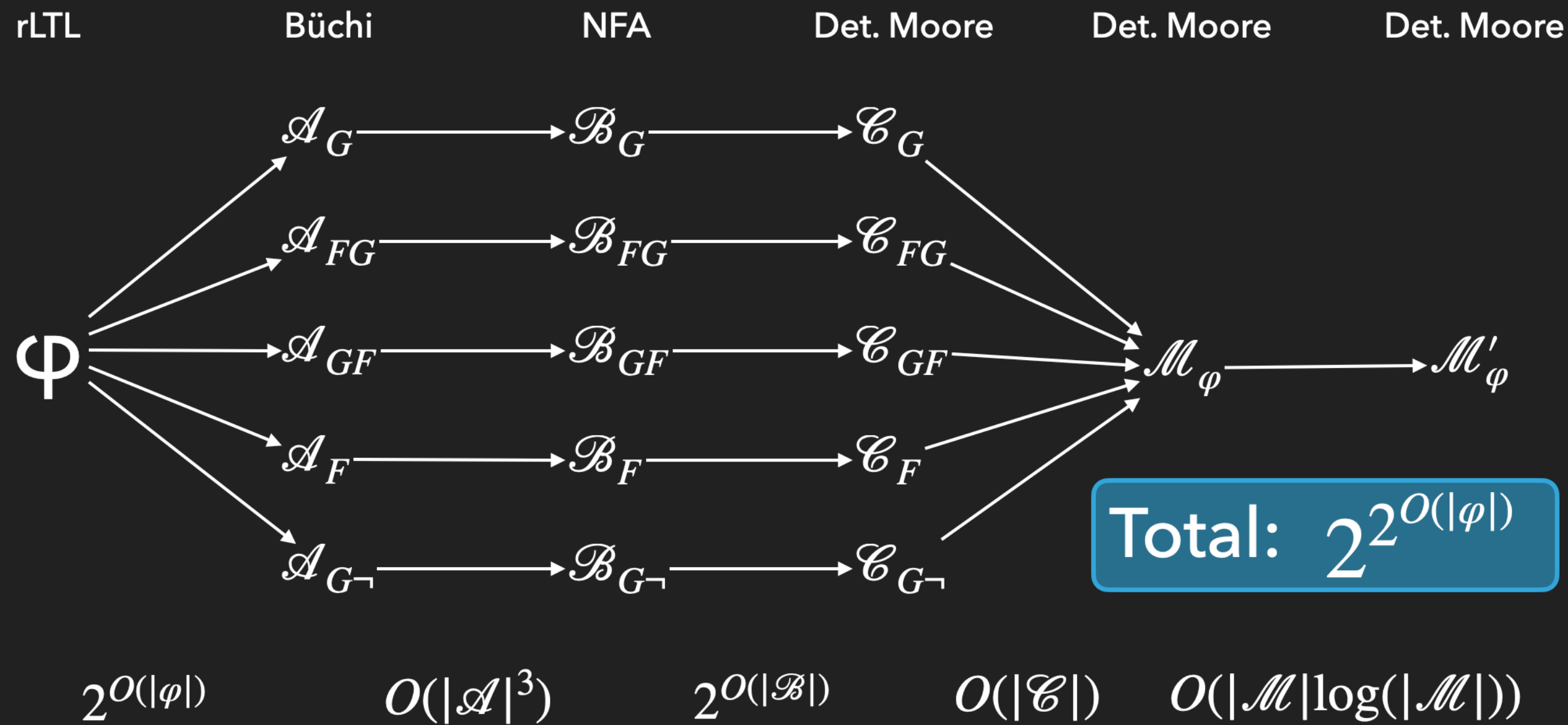


Histogram of the number of monitors with respect to their size

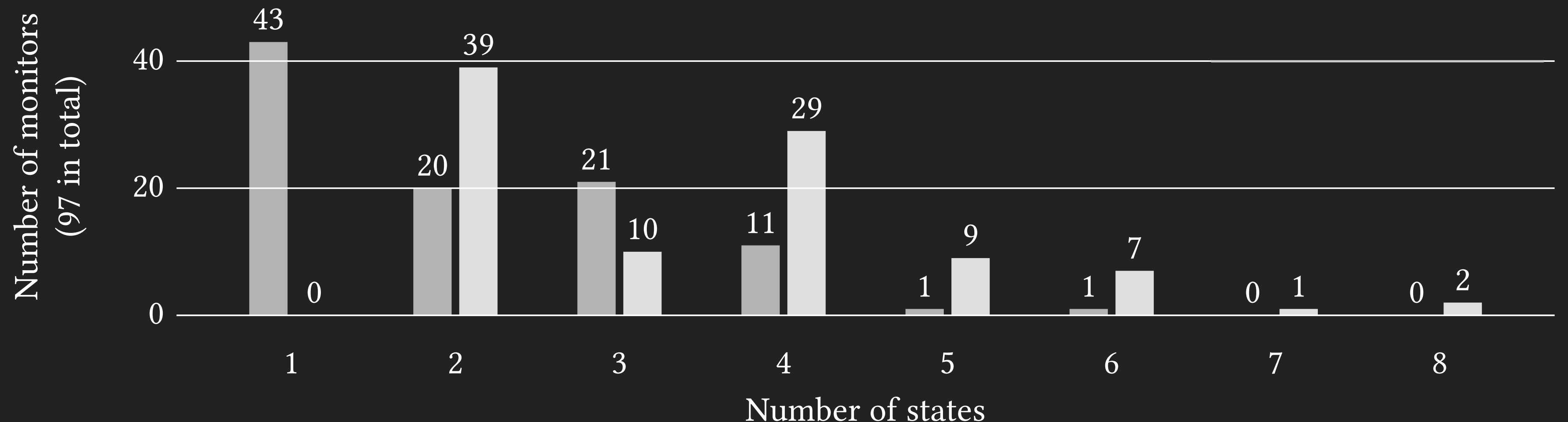


Analysis of the monitor construction for the 54 formulas that are both LTL-monitorable and rLTL-monitorable

Summary



55.7% LTL-monitorable
 versus
100% rLTL-monitorable



Summary



55.7% LTL-monitorable

**From LTL to rLTL:
More Information;
Same (Asymptotic) Cost**

