

CTL* synthesis via LTL synthesis

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in the next 30 minutes

- LTL/CTL* synthesis problem
- Why reduce CTL* synthesis to LTL synthesis?
 - unrealizable specifications
- Reduction
 - annotating trees with strategies
- Conclusion

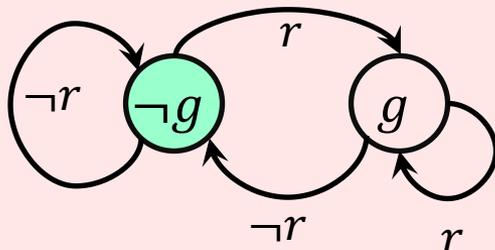
LTL/CTL* synthesis problem by example

Specification:

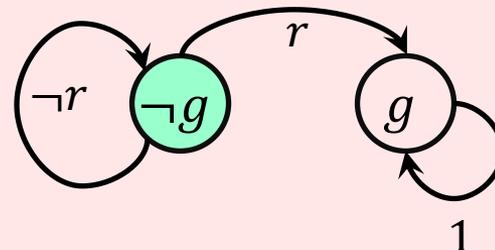
- LTL formula: $G(r \rightarrow F g)$
- Inputs: r , outputs: g

Find a state machine with such inputs/outputs whose all executions satisfy the formula.

An example solution



Another solution



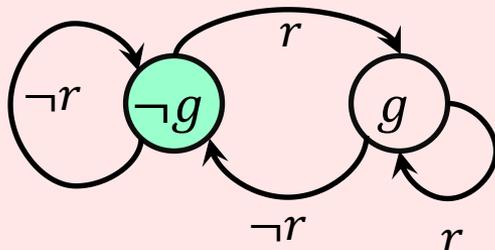
LTL/CTL* synthesis problem by example

Specification:

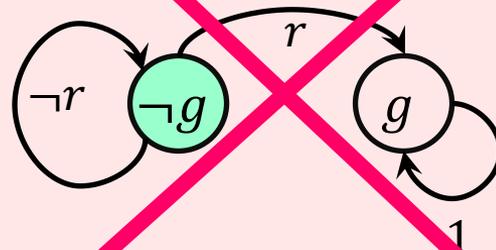
- CTL* formula: $AG(r \rightarrow Fg) \wedge AGEF\neg g$
- Inputs: r , outputs: g

Find a state machine with such inputs/outputs whose all executions satisfy the formula.

An example solution



~~Another solution~~



why reduce CTL* synth. to LTL synthesis?

1. Handle unrealizable CTL* *efficiently*
2. Avoid building specialized CTL* synthesizers
 - re-use state-of-the-art LTL synthesizers

unrealizable specifications: LTL

$[\Phi_{LTL}, I, O, type]$ is unrealizable \Leftrightarrow
 $[\neg\Phi_{LTL}, O, I, \neg type]$ is realizable

Example:

- $g \leftrightarrow \mathbf{X}r$, $I = \{r\}$, $O = \{g\}$ is *unrealizable*.
- $\neg(g \leftrightarrow \mathbf{X}r)$, $I = \{g\}$, $O = \{r\}$ is *realizable*:
output the negated first value of g .

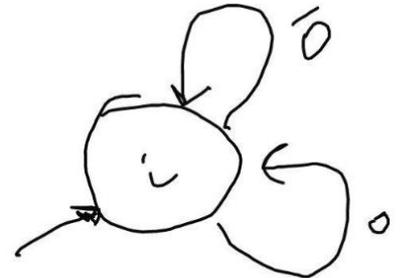
unrealizable specifications: CTL*

$[\Phi_{CTL^*}, I, O, type]$ is unrealizable \Leftrightarrow
 $[\neg\Phi_{CTL^*}, O, I, \neg type]$ is realizable

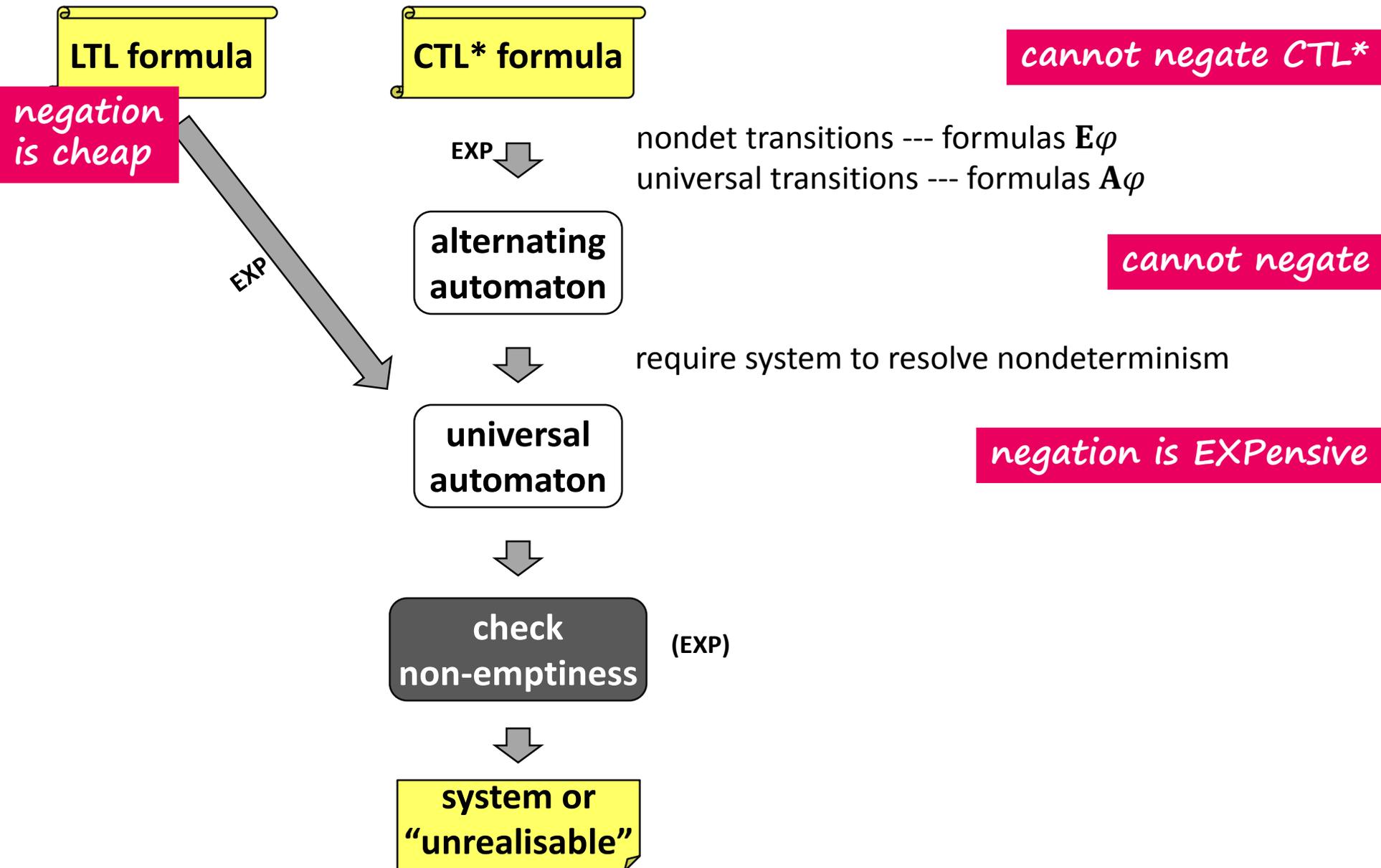
Wrong!

Counterexample:

- $AGo, I = \{i\}, O = \{o\}$ is realizable:
always output o.
- $EF\neg o, I = \{o\}, O = \{i\}$ is realizable:



steps in standard LTL/CTL* synthesis



our reduction

Φ_{CTL^*} is realizable \Leftrightarrow
 Φ_{LTL} is realizable

the *total* blow-up
is as before: EXP

system size can grow

CTL* formula



require system to resolve nondeterminism

LTL formula



universal
automaton



check
non-emptiness

(EXP)

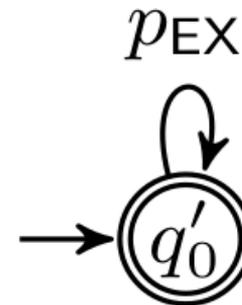


system or
“unrealisable”

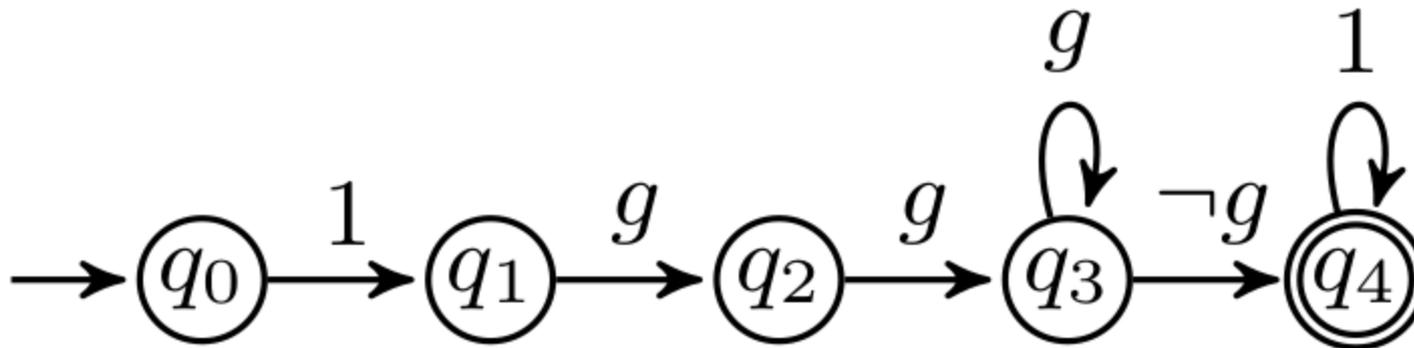
*negation
is cheap*

automata for CTL*

- $\mathbf{EG\ EX}(g \wedge \mathbf{X}(g \wedge \mathbf{F} \neg g))$
- $p_{EX} \equiv \mathbf{EX}(g \wedge \mathbf{X}(g \wedge \mathbf{F} \neg g))$
- $p_{EG} \equiv \mathbf{EG}p_{EX}$

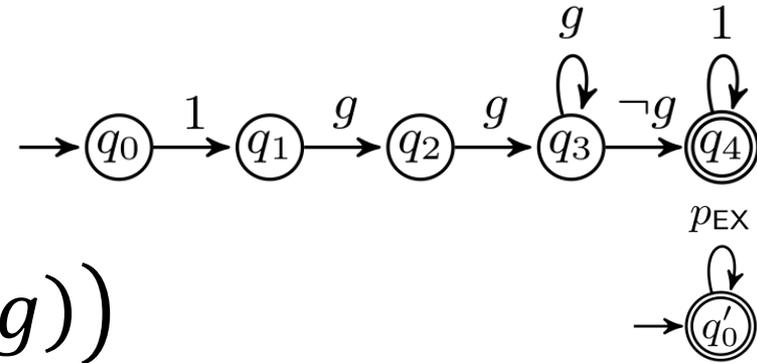


NBW for $\mathbf{G}p_{EX}$



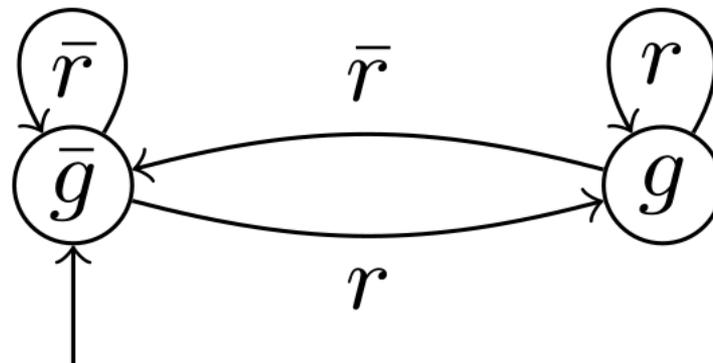
NBW for $\mathbf{X}(g \wedge \dots)$

model checking $\mathbf{EG} \mathbf{EX}(g \wedge \mathbf{X}(g \wedge \mathbf{F} \neg g))$



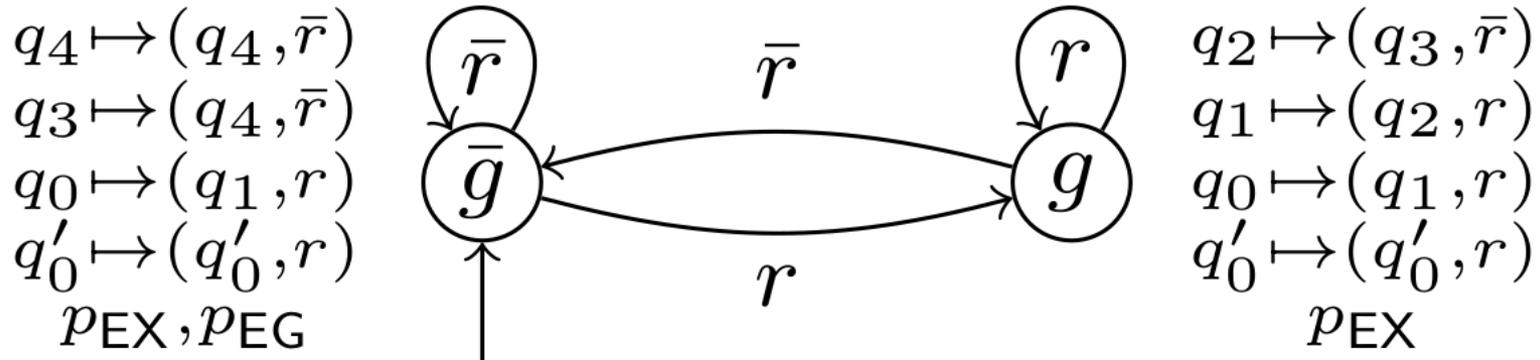
- $p_{EX} \equiv \mathbf{EX}(g \wedge \mathbf{X}(g \wedge \mathbf{F} \neg g))$
- $p_{EG} \equiv \mathbf{EG} p_{EX}$

$q_4 \mapsto (q_4, \bar{r})$
 $q_3 \mapsto (q_4, \bar{r})$
 $q_0 \mapsto (q_1, r)$
 $q'_0 \mapsto (q'_0, r)$
 $p_{EG} \quad p_{EX}$



$q_2 \mapsto (q_3, \bar{r})$
 $q_1 \mapsto (q_2, r)$
 $q'_0 \mapsto (q'_0, r)$
 p_{EX}

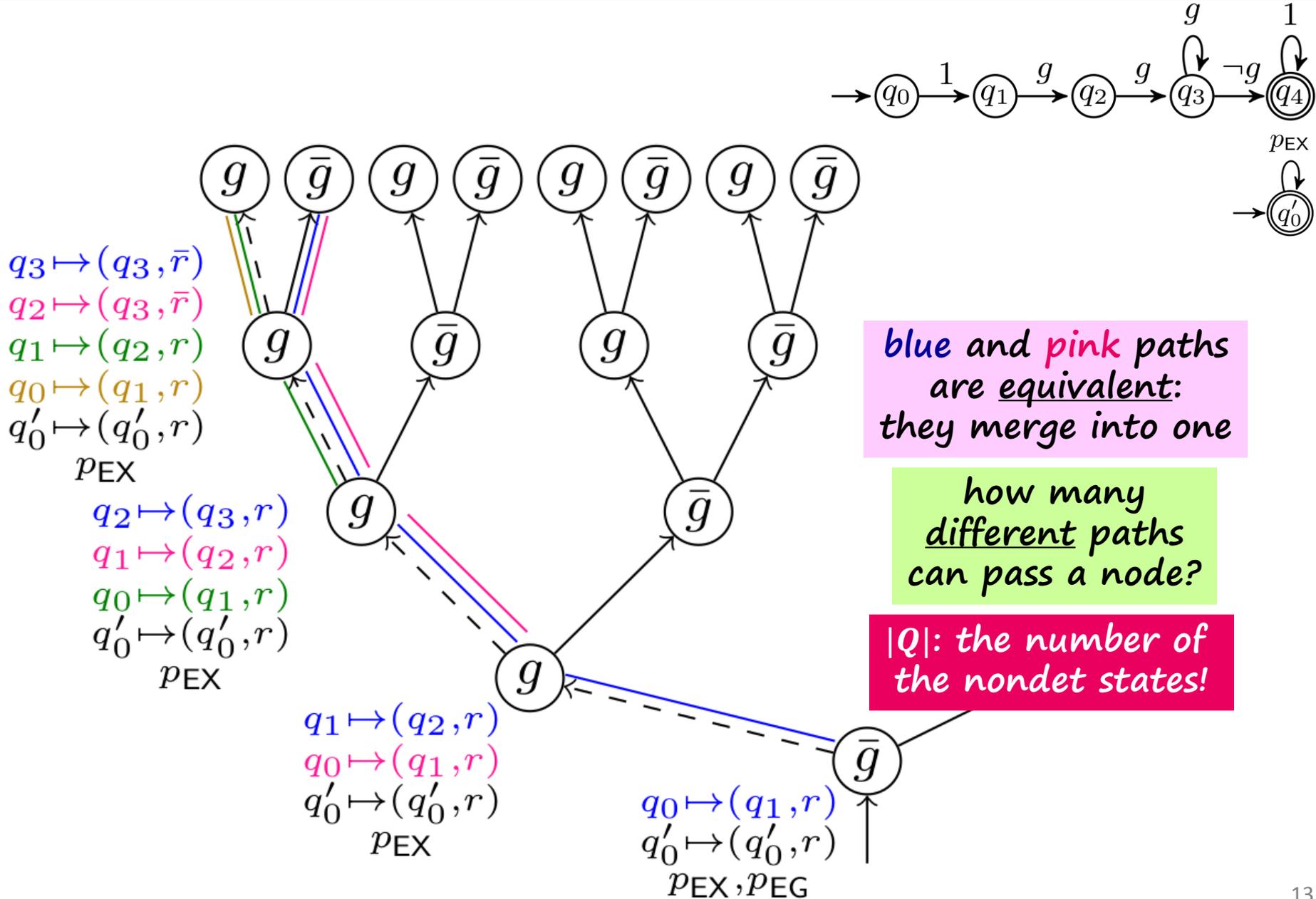
annotated model



Every state is additionally labeled with:

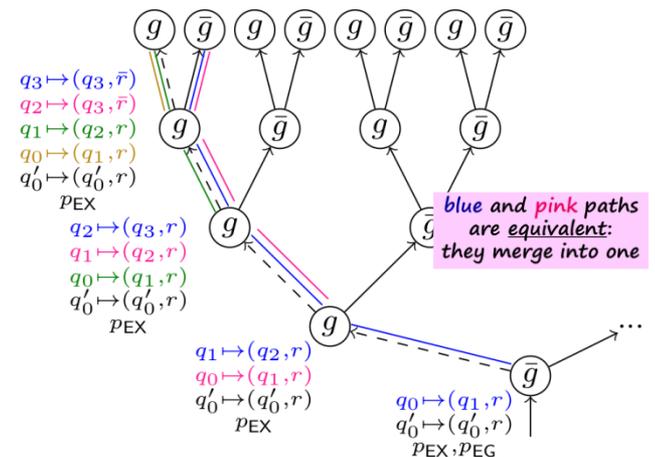
- *subformulas* $\rightarrow \{true, false\}$
- $Q \rightarrow Q \times Direction$

annotated tree

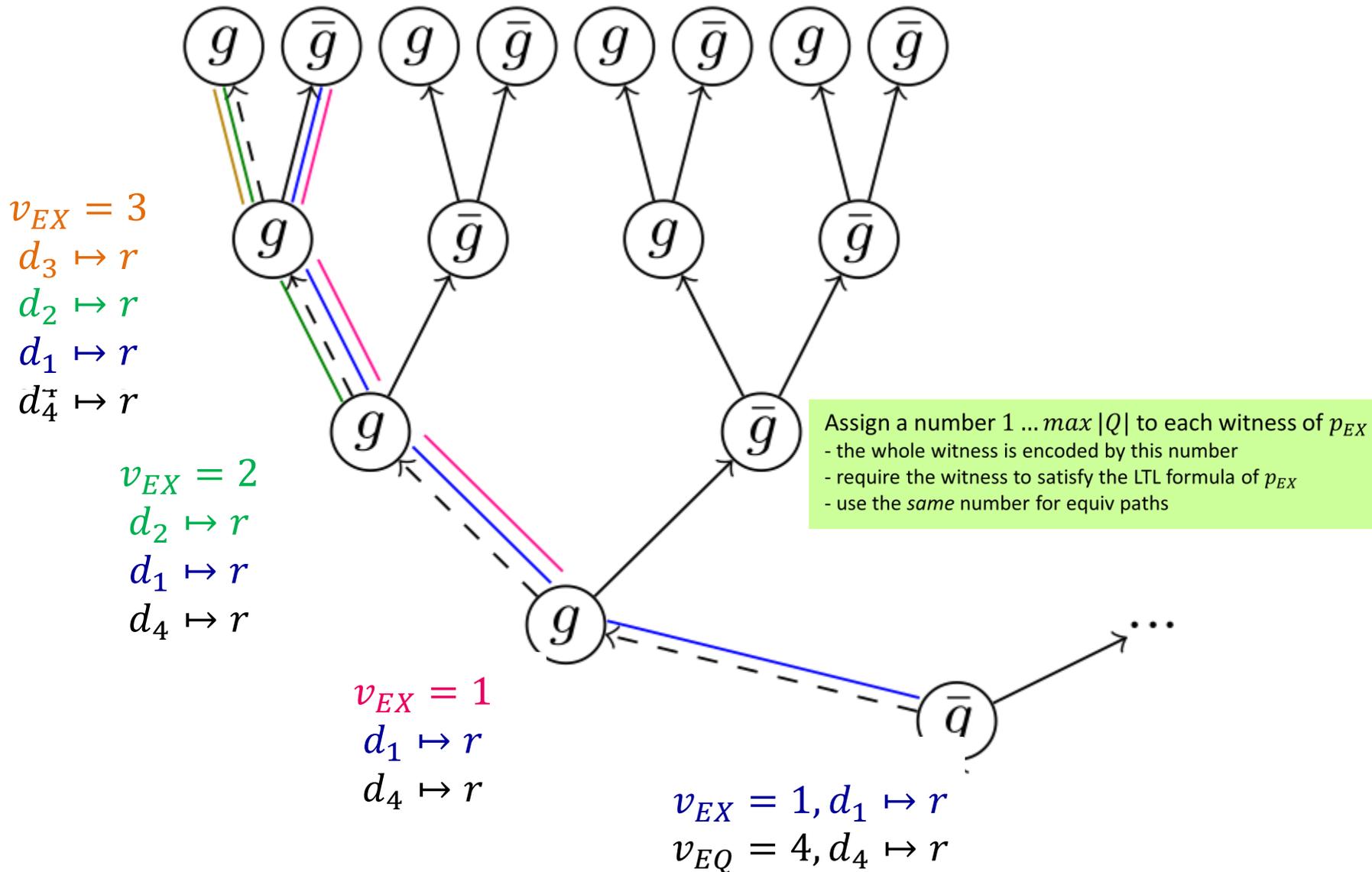


core ideas of reduction

- “merging” paths are *equivalent*
 - max $|Q|$ non-equiv paths can pass through a node
- Assign a number $1 \dots \max |Q|$ to each witness of p_{EX}
 - the whole witness is encoded by this number
 - require the witness to satisfy the LTL formula of p_{EX}
 - use the *same* number for equiv paths



newly annotated tree



LTL formula

- For each subformula $E\varphi$:

$$\bigwedge_{i \in \{1 \dots |Q|\}} \mathbf{G} [v_{E\varphi} = i \rightarrow (\mathbf{G}d_i \rightarrow \varphi')] \quad (1)$$

- For each subformula $A\varphi$:

$$\mathbf{G} [p_{A\varphi} \rightarrow \varphi'] \quad (2)$$

- The LTL formula is

$$\bigwedge_{E\varphi} Eq. 1 \wedge \bigwedge_{A\varphi} Eq. 2$$

our result

- For each subformula $E\varphi$:

$$\bigwedge_{i \in \{1 \dots |Q|\}} \mathbf{G}[v_{E\varphi} = i \rightarrow (\mathbf{G}d_i \rightarrow \varphi')]$$

- For each subformula $A\varphi$:

$$\mathbf{G}[p_{A\varphi} \rightarrow \varphi']$$

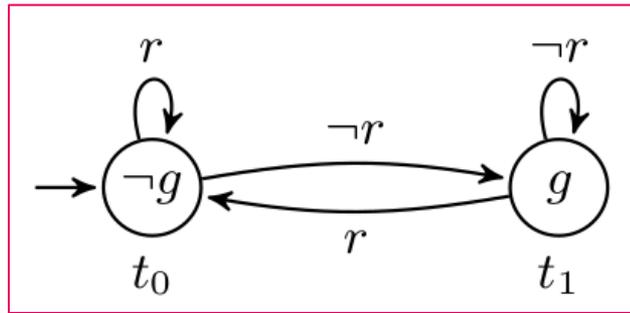
- The LTL formula is

$$\bigwedge_{E\varphi} Eq.1 \wedge \bigwedge_{A\varphi} Eq.2$$

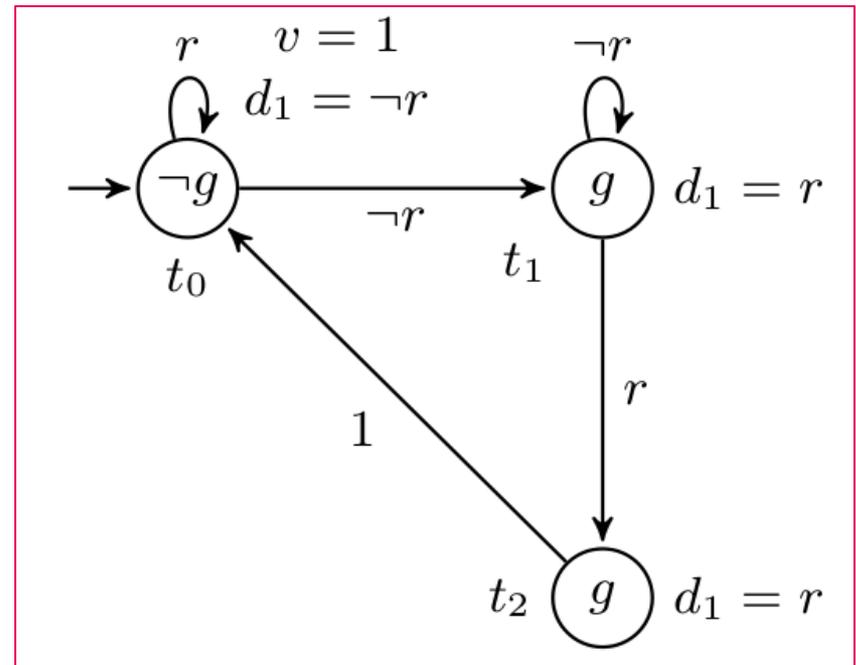
- Φ_{LTL} is realizable $\Leftrightarrow \Phi_{CTL^*}$ is realizable
- The complexity stays in 2EXP
- The system can get larger!

example: $\text{EX}(g \wedge \mathbf{X}(g \wedge \mathbf{X}\neg g))$

$$v \neq 0 \wedge \bigwedge_{i \in \{1, \dots, 5\}} \mathbf{G}[v = i \rightarrow (\mathbf{G}d_i \rightarrow \mathbf{X}(g \wedge \mathbf{X}(g \wedge \mathbf{X}\neg g)))]$$



a smallest system satisfying Φ_{CTL^*}



a smallest system satisfying Φ_{LTL}

conclusion

We reduced CTL* synthesis to LTL synthesis without incurring a blow up.

Now we can use the reduction to handle unrealizable CTL* specifications and to re-use LTL synthesizers.

