Distributed synthesis

Partition the classes of architectures

Decidability of  $\mathcal{D}$  Undecidability of  $\mathcal{U}$ 

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## **Distributed Synthesis**

Jonathan Türpe

June 26, 2008

Distributed synthesis

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# Introduction

**Distributed synthesis** 

Partition the classes of architectures

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## What we did so far

[Andreas Augustin, Synthesis under incomplete information]

#### Synthesis problem

• Decide if a given specification has an implementation

#### **Closed synthesis:**

• single-process, no interaction with the environment

#### Open synthesis:

- single-process, interaction with the environment
- solved for CTL\*

#### Open synthesis with incomplete information:

- single-process, interaction with the environment, hidden variables
- solved for CTL\* [Kup.Var97]

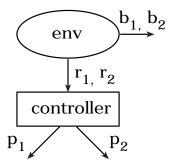
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## Example: Incomplete information

[Andreas Augustin, Synthesis under incomplete information]



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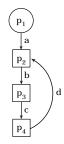
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## Distributed synthesis

#### Distributed synthesis

- Multiple processes
- Interaction between the processes



#### Distributed synthesis problem

- Given: A specification  $\varphi$  and an architecture A
- Decide if there are implementations such that the composition satisfies  $\varphi$

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Partition the classes of architectures

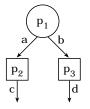
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## History of distributed synthesis

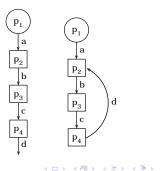
Negative result [Pnu.Ros90]

• Specification in LTL



Positive results [Kup.Var01]

- Specification in CTL\*
- Pipeline-, ring architectures



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## Distributed synthesis

#### Generalized result [Fin.Sch05]

- Can partition architectures in two clases  ${\cal D}$  and  ${\cal U}$
- Positiv result
  - Specification  $\varphi$  in CTL\*
  - Architectures in class  $\mathcal D$
- Negativ result
  - Specification in LTL, or CTL
  - Architectures in class  $\mathcal U$

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## Distributed synthesis

Distributed synthesis

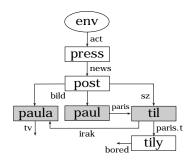
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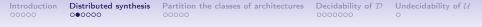
## Definitions

### Architecture $A=(P, W, p_{env}, E, O, H)$

- P set of processes
- $W \subset P$  white-box processes
- $p_{env} \in P \setminus W$
- (P, E) directed graph
- $O = \{O_e \mid e \in E\}$
- $H = \{H_p \mid p \in P\}$
- Notation: B = P \ W for black-box processes



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## Definitions

#### Implementation of a process p

- Is a function  $s_p:(2^{l_p})^* \to 2^{O_p}$  called *strategy*
- $I_p$  and  $O_p$  are the input or output variables of p

#### Implementation of an architecture A

• Set of strategies  $S = \{s_p \mid p \in B \setminus p_{env}\}$ 

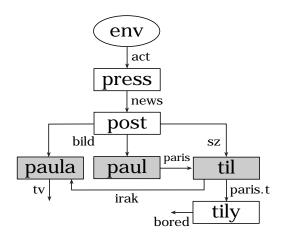
Composition of a set of strategies

- Function  $s_Q:(2^{I_Q})^* \rightarrow 2^{O_Q}$
- $I_Q$  and  $O_Q$  is the common input or output respectively

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Mapping according to its single strategies





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## Definitions

#### Computation tree of an implementation S

• Is a 
$$2^{O \cup H}$$
 labeled  $2^{O_{env}}$ -tree

• 
$$< 2^{O_{env}*}, \ell >= xray_2o_{env}(wide_2H_{env}(<(2^{O_{env}\setminus H_{env}})^*, s_{B\setminus p_{env}}>))$$

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### Definitions

#### Distributed synthesis problem

- Given a tuple (A, φ), consisting of an architecture A and a specification φ
- (A, φ) is *realizable* if there exists an implementation of A s.t. its computational tree satisfies φ.
- An architecture is called *decidable* if there exists an algorithm that decides for all specifications  $\varphi$  if  $(A, \varphi)$  is realizable.

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Distributed synthesis

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## Recapitulation of the definitions

- Architecture
- Implementation
- Computation tree
- Distributed synthesis problem
- Decidability of an architecture

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## Partition the classes of architectures

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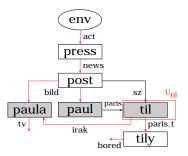
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## Informedness of processes

#### Defining the preorder $p \preccurlyeq p'$

• Reading: p has more or equal information than p'



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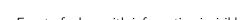
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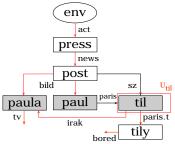
## Informedness of processes

#### Defining the preorder $p \preccurlyeq p'$

- Reading: p has more or equal information than p'
- $E_p = \{e \in E \mid O_e \not\subseteq I_p\}$
- U<sub>p</sub> = {q ∈ B | ¬∃ directed path from p<sub>env</sub> to q in (P, E<sub>p</sub>)}
- $p \preccurlyeq p' \leftrightarrow p' \in U_p$  for  $p, p' \in B$



- *E<sub>p</sub>* set of edges with information invisible to p
- $U_p$  processes that are not reachable by this edges



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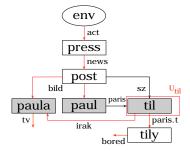
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- $E_p$  set of edges with information invisible to p
- $U_p$  processes that are not reachable by this edges
- Example : til ≼ til

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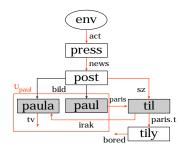
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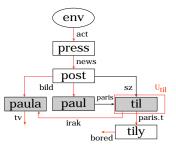
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### Example

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## Informdness of processes

#### An architecture A is called ordered

• Exists a surjective function  $f: B \to \mathbb{N}_n$ 

• 
$$f^{-1}(1) = \{p_{env}\}$$

For all p, p' ∈ B : f(p) ≤ f(p') iff. p ≼ p'
 i.e. p has more or equal information than p'

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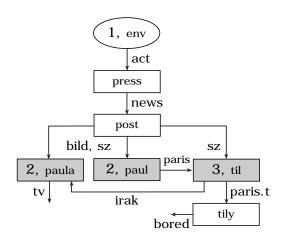
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## Informdness of processes

#### An architecture A is called strictly ordered

- Exists a bijective function  $f: B \to \mathbb{N}_n$
- $f^{-1}(1) = \{p_{env}\}$
- For all p, p' ∈ B : f(p) ≤ f(p') iff p ≼ p'
   i.e. p has more or equal information than p'
- ${\mathcal D} \text{ and } {\mathcal U}$ 
  - An architecture A is in  $\mathcal{D}$  if A can be ordered
  - An architecture A is in  $\mathcal{U}$  otherwise

#### Example



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## Decidability of ordered architectures

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## Architecture transformation

Consider only ordered architectures A.

We can equivalently transform the decidability problem from  $(A, \varphi)$  to  $(A', \varphi')$ .

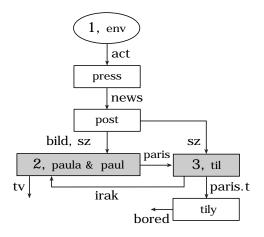
- 1. Clustering equally informed processes
- 2. Elimination of white-box processes
- 3. Elimination of feedback edges

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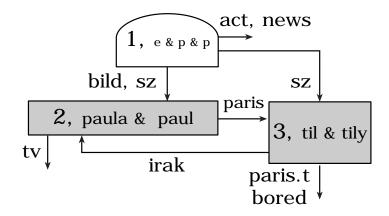
## Clustering equally informed processes



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#### Elimination of white-box processes



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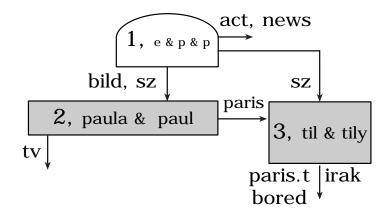
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Undecidability of  $\mathcal{U}$ 

### Elimination of feedback edges



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#### Automaton construction

#### Idea:

Given the transformed distributed synthesis problem  $(A', \varphi')$ . (A' is an strictly ordered architecture with no white-boxes and no feedback edges and  $\varphi'$  a CTL\*-formula)

Construct an alternating automaton  $\mathcal A$  that recognizes a tree iff it is a computational tree of A' that satisfies  $\varphi'$ .

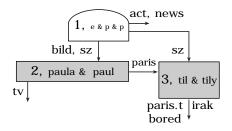
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Undecidability of  $\mathcal{U}$ 

#### Automaton construction



1.  $\varphi \rightsquigarrow \mathcal{A}_1$  over  $2^{\{a,n,b,s,t,p,p',i,b\}}$ -labeled  $2^{\{a,n,b,s\}}$ -trees

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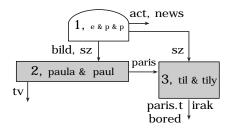
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#### Automaton construction



 $\begin{array}{l} 1. \hspace{0.1 cm} \varphi \rightsquigarrow \mathcal{A}_1 \hspace{0.1 cm} \text{over} \hspace{0.1 cm} 2^{\{a,n,b,s,t,p,p',i,b\}} \text{-labeled} \hspace{0.1 cm} 2^{\{a,n,b,s\}} \text{-trees} \\ \\ 2. \hspace{0.1 cm} \mathcal{A}_1 \rightsquigarrow \mathcal{A}_2 \hspace{0.1 cm} \text{over} \hspace{0.1 cm} 2^{\{t,p,p',i,b\}} \text{-labeled} \hspace{0.1 cm} 2^{\{a,n,b,s\}} \text{-trees} \end{array}$ 

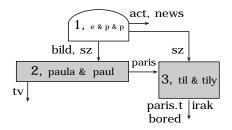
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#### Automaton construction



1.  $\varphi \rightsquigarrow \mathcal{A}_1$  over  $2^{\{a,n,b,s,t,p,p',i,b\}}$ -labeled  $2^{\{a,n,b,s\}}$ -trees 2.  $\mathcal{A}_1 \rightsquigarrow \mathcal{A}_2$  over  $2^{\{t,p,p',i,b\}}$ -labeled  $2^{\{a,n,b,s\}}$ -trees 3.  $\mathcal{A}_2 \rightsquigarrow \mathcal{A}_3$  over  $2^{\{t,p,p',i,b\}}$ -labeled  $2^{\{b,s\}}$ -trees

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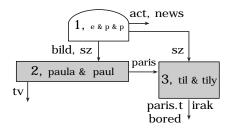
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#### Automaton construction



1.  $\varphi \rightsquigarrow \mathcal{A}_1$  over  $2^{\{a,n,b,s,t,p,p',i,b\}}$ -labeled  $2^{\{a,n,b,s\}}$ -trees 2.  $\mathcal{A}_1 \rightsquigarrow \mathcal{A}_2$  over  $2^{\{t,p,p',i,b\}}$ -labeled  $2^{\{a,n,b,s\}}$ -trees 3.  $\mathcal{A}_2 \rightsquigarrow \mathcal{A}_3$  over  $2^{\{t,p,p',i,b\}}$ -labeled  $2^{\{b,s\}}$ -trees 4.  $\mathcal{A}_3 \rightsquigarrow \mathcal{N}_3$  nondeterministic automaton

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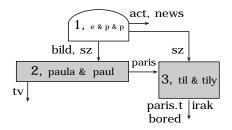
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#### Automaton construction



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### Decidability of $\mathcal{D}$

#### Theorem.

Let A be an ordered architecture and  $\varphi$  a CTL\*-formula.

The distributed synthesis problem for  $(A, \varphi)$  is decidable.

Distributed synthesis

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Decidability of  $\mathcal{D}$ 0000000 Undecidability of  $\mathcal{U}_{\odot}$ 

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## Undecidability of architectures that can not be ordered

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### Undecidability of $\mathcal{A}_2$

**Theorem.** Let A be an architecture, s.t. A can not be ordered and  $\varphi$  a CTL or LTL specification.

The distributed synthesis problem for  $(A, \varphi)$  is undecidable.

Proof idea: Using a reduction from the halting problem.

Thank you for your attention.

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Introduction 00000	Distributed synthesis	Partition the classes of architectures	Decidability of $\mathcal{D}$	Undecidability of $\mathcal U$

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