

Timed Interfaces

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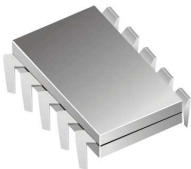
Seminar Games in Verification and Synthesis
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original paper by

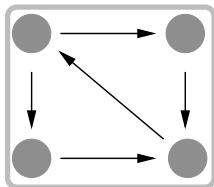
L. de Alfaro, T. Henzinger and M. Stoelinga

Motivation

Component



Interface

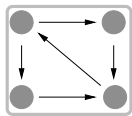


Timing Requirements

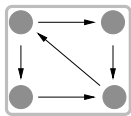


- ▶ complex real-time systems
- ▶ component based design
- ▶ interface describes component behavior

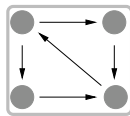
Motivation



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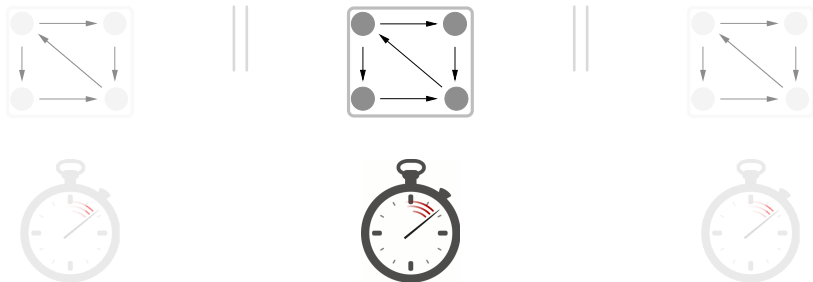


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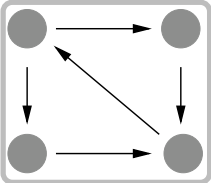
- ▶ model component interaction
- ▶ type system for interfaces

Motivation



- ▶ well-formed?
- ▶ input assumptions: expected use
- ▶ output guarantees: correct input \Rightarrow correct output

Motivation

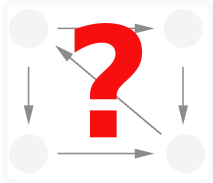


System

well-formed

||

vs.



Environment

\exists Environment
that fulfils input
assumptions

\Leftrightarrow

Timed Interface Theory

We are interested in

- ▶ Well-formedness
- ▶ Compatibility
- ▶ Composition

Talk Outline

Composition and Compatibility

Timed Interfaces as Timed Games

Timed Interface Automata

Solving Timed Games

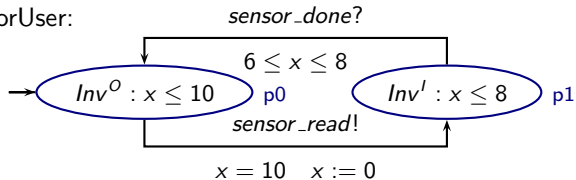
Definition: Timed Interface

Timed interface $\mathcal{P} = (S_{\mathcal{P}}, s_{\mathcal{P}}^{init}, Acts_{\mathcal{P}}^I, Acts_{\mathcal{P}}^O, \rho_{\mathcal{P}}^I, \rho_{\mathcal{P}}^O)$ with:

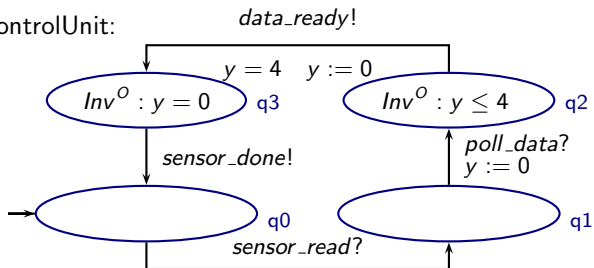
- ▶ $S_{\mathcal{P}}$: set of states
- ▶ $s_{\mathcal{P}}^{init} \in S_{\mathcal{P}}$: initial state
- ▶ $Acts_{\mathcal{P}}^I$ and $Acts_{\mathcal{P}}^O$: immediate input/output actions
- ▶ \mathbb{T} : set of timed actions ($\mathbb{T} = \mathbb{R}_{\geq 0}$ or $\mathbb{T} = \mathbb{N}$)
- ▶ $Acts_{\mathcal{P}} = Acts_{\mathcal{P}}^I \cup Acts_{\mathcal{P}}^O$
- ▶ $\Gamma_{\mathcal{P}}^I = Acts_{\mathcal{P}}^I \cup \mathbb{T}$: set of all input actions
- ▶ $\Gamma_{\mathcal{P}}^O = Acts_{\mathcal{P}}^O \cup \mathbb{T}$: set of all output actions
- ▶ $\rho_{\mathcal{P}}^I \subseteq S_{\mathcal{P}} \times \Gamma_{\mathcal{P}}^I \times S_{\mathcal{P}}$: input transition relation
- ▶ $\rho_{\mathcal{P}}^O \subseteq S_{\mathcal{P}} \times \Gamma_{\mathcal{P}}^O \times S_{\mathcal{P}}$: output transition relation

Example

SensorUser:



SensorControlUnit:



Composability

Timed interfaces \mathcal{P} and \mathcal{Q} are composable if

- ▶ \mathcal{P} and \mathcal{Q} are well-formed
- ▶ $Acts_{\mathcal{P}}^O \cap Acts_{\mathcal{Q}}^O = \emptyset$

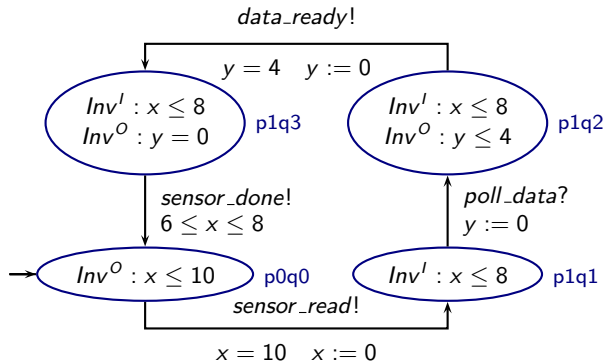
Shared actions: $shared(\mathcal{P}, \mathcal{Q}) := Acts_{\mathcal{P}} \cap Acts_{\mathcal{Q}}$

Interface Product

For \mathcal{P} and \mathcal{Q} composable timed interfaces

- ▶ $S_{\mathcal{P} \otimes \mathcal{Q}} = S_{\mathcal{P}} \times S_{\mathcal{Q}}$
- ▶ $s_{\mathcal{P} \otimes \mathcal{Q}}^{init} = (s_{\mathcal{P}}^{init}, s_{\mathcal{Q}}^{init})$.
- ▶ $Acts_{\mathcal{P} \otimes \mathcal{Q}}^I = Acts_{\mathcal{P}}^I \cup Acts_{\mathcal{Q}}^I \setminus shared(\mathcal{P}, \mathcal{Q})$
- ▶ $Acts_{\mathcal{P} \otimes \mathcal{Q}}^O = Acts_{\mathcal{P}}^O \cup Acts_{\mathcal{Q}}^O$.
- ▶ $\rho_{\mathcal{P} \otimes \mathcal{Q}}^I = \{((s_1, s_2), \alpha, (s'_1, s'_2)) \mid (s_1, \alpha, s'_1) \in \rho_{\mathcal{P}}^I \text{ and } (s_2, \beta, s'_2) \in \rho_{\mathcal{Q}}^I, \beta = \alpha \text{ if } \alpha \in Acts_{\mathcal{Q}}^I \text{ or } \beta = 0 \text{ otherwise}\} \cup \{((s_1, s_2), \alpha, (s'_1, s'_2)) \mid (s_2, \alpha, s'_2) \in \rho_{\mathcal{Q}}^I \text{ and } (s_1, \beta, s'_1) \in \rho_{\mathcal{P}}^I, \beta = \alpha \text{ if } \alpha \in Acts_{\mathcal{P}}^I \text{ or } \beta = 0 \text{ otherwise}\}$
- ▶ $\rho_{\mathcal{P} \otimes \mathcal{Q}}^O = \{((s_1, s_2), \alpha, (s'_1, s'_2)) \mid (s_1, \alpha, s'_1) \in \rho_{\mathcal{P}}^O \text{ and } (s_2, \beta, s'_2) \in \rho_{\mathcal{Q}}^O, \beta = \alpha \text{ if } \alpha \in Acts_{\mathcal{Q}}^O \text{ or } \beta = 0 \text{ otherwise}\} \cup \{((s_1, s_2), \alpha, (s'_1, s'_2)) \mid (s_2, \alpha, s'_2) \in \rho_{\mathcal{Q}}^O \text{ and } (s_1, \beta, s'_1) \in \rho_{\mathcal{P}}^I, \beta = \alpha \text{ if } \alpha \in Acts_{\mathcal{P}}^I \text{ or } \beta = 0 \text{ otherwise}\}$

Example II



SensorUser \otimes SensorControlUnit

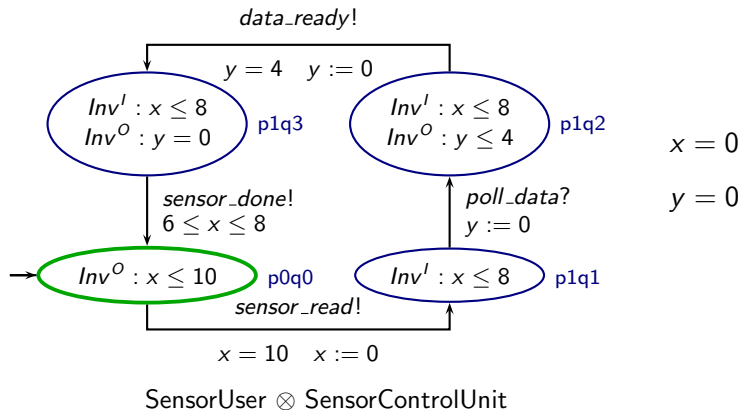
Error States

Problems with interface product:

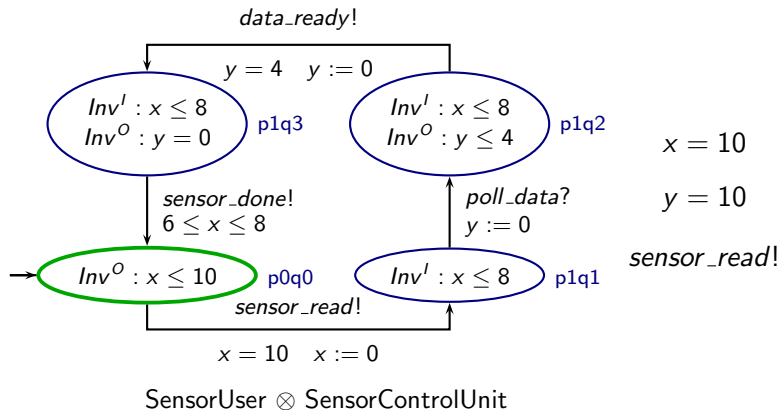
- ▶ timing requirements of components not synchronized
- ▶ one component could create output that cannot be accepted

⇒ error state

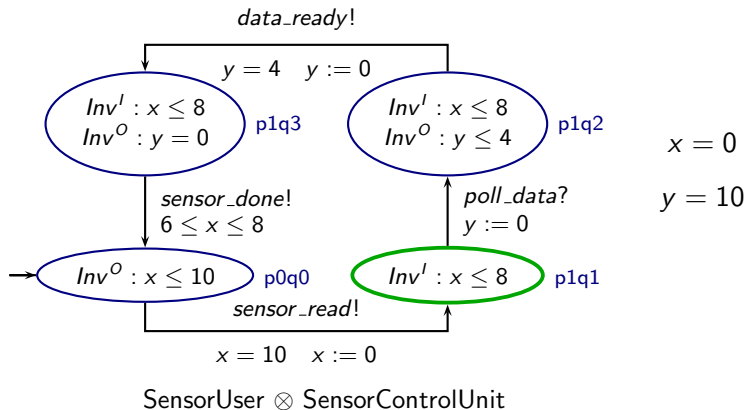
Example III



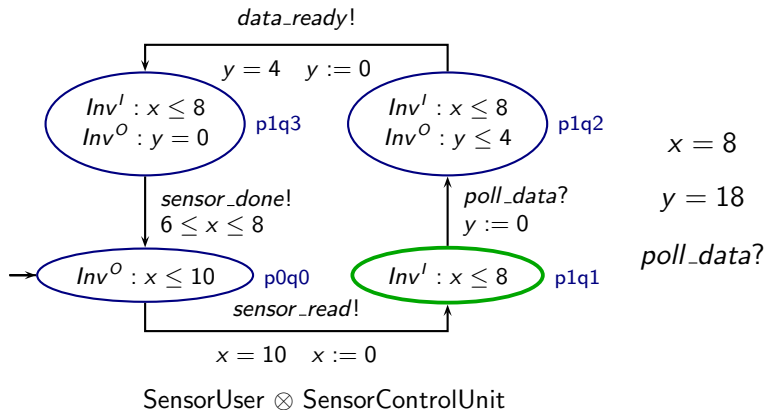
Example III



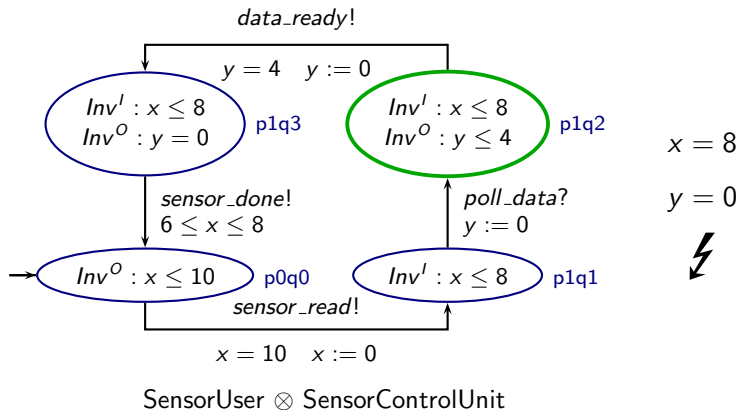
Example III



Example III



Example III



Timed Interfaces as Timed Games [dAHS02]

- ▶ model interface as two-player game
- ▶ Player Input: Environment
- ▶ Player Output: Component
- ▶ Moves: timed actions wait for event
 immediate actions trigger event
- ▶ goal: time diverges or other player blocks

Definition: Moves and Outcome

Possible moves for player $\gamma \in \{I, O\}$ in state $s \in \mathcal{S}_P$

- ▶ $\Gamma_P^\gamma(s) = \{\alpha \in \Gamma_P^\gamma \mid \exists s' \in \mathcal{S}_P. (s, \alpha, s') \in \rho_P^\gamma\}$
- ▶ $\Gamma_P^\gamma(s) \neq \emptyset \implies (s, 0, s) \in \rho_P^\gamma$
- ▶ $\Gamma_P^\gamma(s) = \emptyset \implies$ player γ loses because he blocks

For $s \in \mathcal{S}_P$, $\alpha_I \in \Gamma_P^I(s)$, $\alpha_O \in \Gamma_P^O(s)$ and $bl \in \{I, O\}$, define outcome $\delta_P(s, \alpha_I, \alpha_O) := (\alpha, s', bl)$

- ▶ $\alpha_I, \alpha_O \in \mathbb{T} \implies \alpha = \min\{\alpha_I, \alpha_O\}$
 $bl = I$ if $\alpha_I < \alpha_O$, $bl = O$ otherwise (asymmetric!).
- ▶ If $\alpha_I \in \text{Acts}_P$ and $\alpha_O \in \mathbb{T}$, then $\alpha = \alpha_I$ and $bl = I$.
- ▶ If $\alpha_I \in \mathbb{T}$ and $\alpha_O \in \text{Acts}_P$, then $\alpha = \alpha_O$ and $bl = O$.
- ▶ If $\alpha_I, \alpha_O \in \text{Acts}_P$, choose $\alpha = \alpha_I$ and $bl = I$ or $\alpha = \alpha_O$ and $bl = O$ nondeterministically.

Definition: Strategies and Reachable States

Strategy $\pi^\gamma : S_{\mathcal{P}}^* \rightarrow \Gamma_{\mathcal{P}}^\gamma$ for player $\gamma \in \{I, O\}$ assigns move $\pi^\gamma(\bar{s}) \in \Gamma_{\mathcal{P}}^\gamma(s)$ to every $\bar{s} \in S_{\mathcal{P}}^*$ whose final state is s , if $\Gamma_{\mathcal{P}}^\gamma \neq \emptyset$. Otherwise, $\pi^\gamma(\bar{s})$ is undefined.

State $s \in S_{\mathcal{P}}$ is reachable if there are strategies π^I and π^O for player I and O s.t s is visited during game starting from $s_{\mathcal{P}}^{init}$ that is played according to π^I and π^O .

Well-formedness

Liveness

- ▶ sum of timed actions must not converge (no Zeno behavior [MPS95])
- ▶ player must not block game

Blocking

- ▶ player runs out of moves
- ▶ one player always plays, but time does not converge

A timed interface is well-formed if there is strategy for both players to let time diverge or blame the other player for blocking the game.

Combining Interfaces

- ▶ one component might produce output that cannot be accepted by others \Rightarrow error state
- ▶ optimistic approach: restrict interface to make it work
- ▶ can't change components \Rightarrow change use (environment)
- ▶ guarantee safety by avoiding error states

Error States

Immediate error state:

$(s, t) \in S_{\mathcal{P} \otimes \mathcal{Q}}$ with $\alpha \in \text{shared}(\mathcal{P}, \mathcal{Q})$ such that

$\exists s' : (s, \alpha, s') \in \rho_{\mathcal{P}}^O$ and $\forall t' : (t, \alpha, t') \notin \rho_{\mathcal{Q}}^I$ or

$\exists t' : (t, \alpha, t') \in \rho_{\mathcal{Q}}^O$ and $\forall s' : (s, \alpha, s') \notin \rho_{\mathcal{P}}^I$.

set of all immediate error states: $i\text{-errors}(\mathcal{P}, \mathcal{Q}) \subseteq S_{\mathcal{P} \otimes \mathcal{Q}}$.

Time error state:

$(s, t) \in S_{\mathcal{P} \otimes \mathcal{Q}}$ reachable in $\mathcal{P} \otimes \mathcal{Q}$, but there is no strategy to win the game for player I in $S_{\mathcal{P} \otimes \mathcal{Q}} \setminus i\text{-errors}(\mathcal{P}, \mathcal{Q})$.

set of all time error states: $t\text{-errors}(\mathcal{P}, \mathcal{Q})$

Interface Composition

well-formed, composable interfaces \mathcal{P} and \mathcal{Q} are compatible
if $(s_{\mathcal{P}}^{init}, s_{\mathcal{Q}}^{init}) \notin t\text{-errors}(\mathcal{P}, \mathcal{Q})$

composition $\mathcal{P} \parallel \mathcal{Q}$ defined like $\mathcal{P} \otimes \mathcal{Q}$

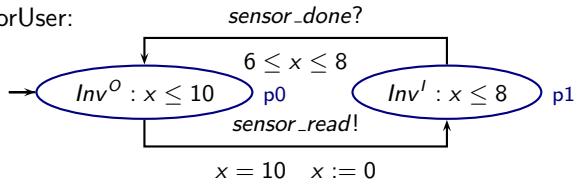
except for input transition relation:

$$U = S_{\mathcal{P} \otimes \mathcal{Q}} \setminus t\text{-errors}(\mathcal{P}, \mathcal{Q})$$

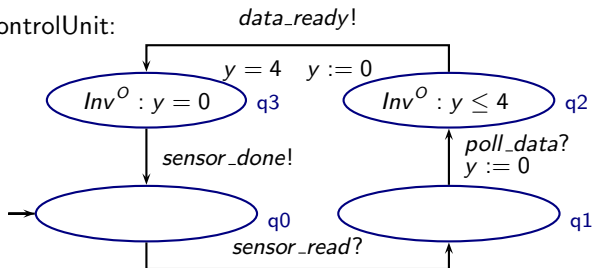
$$\rho_{\mathcal{P} \parallel \mathcal{Q}}^I = \rho_{\mathcal{P} \otimes \mathcal{Q}}^I \cap (U \times \text{Acts}_{\mathcal{P} \otimes \mathcal{Q}}^I \times U)$$

Examples revisited

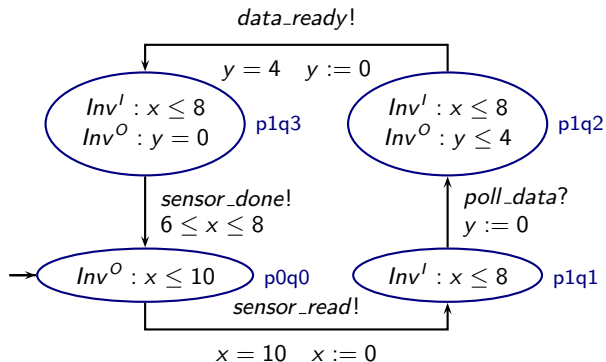
SensorUser:



SensorControlUnit:

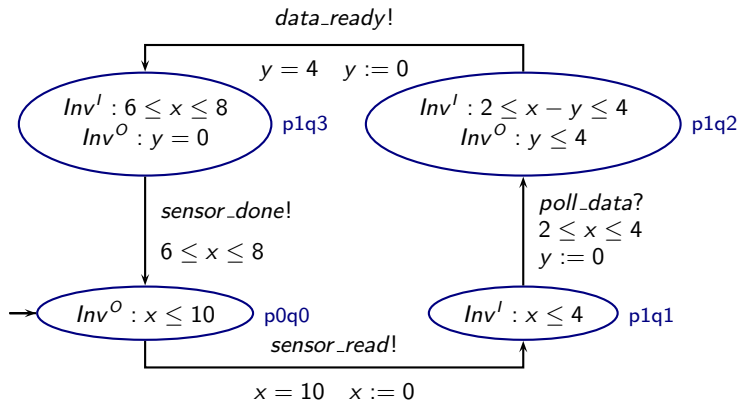


Examples revisited



SensorUser \otimes SensorControlUnit

Examples revisited



$\text{SensorUser} \parallel \text{SensorControlUnit}$

Timed Interface Automata

- ▶ finite representation for timed interfaces
- ▶ similar to timed automata ([AD94])
- ▶ reuse existing algorithms for calculating live states, composition and checking well-formedness

Definition: Timed Interface Automata

Timed interface automaton

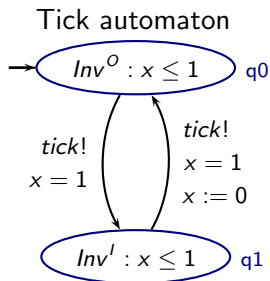
$\mathcal{A} = (Q_{\mathcal{A}}, q_{\mathcal{A}}^{init}, \mathcal{X}_{\mathcal{A}}, Acts_{\mathcal{A}}^I, Acts_{\mathcal{A}}^O, Inv_{\mathcal{A}}^I, Inv_{\mathcal{A}}^O, \rho_{\mathcal{A}})$ with:

- ▶ $Q_{\mathcal{A}}$: set of *locations*
- ▶ $q_{\mathcal{A}}^{init} \in Q_{\mathcal{A}}$: initial location
- ▶ $\mathcal{X}_{\mathcal{A}}$: set of clocks
- ▶ $Acts_{\mathcal{A}}^I$ and $Acts_{\mathcal{A}}^O$: sets of input and output actions
- ▶ $Inv_{\mathcal{A}}^I : Q_{\mathcal{A}} \mapsto \Xi[\mathcal{X}_{\mathcal{A}}]$ and $Inv_{\mathcal{A}}^O : Q_{\mathcal{A}} \mapsto \Xi[\mathcal{X}_{\mathcal{A}}]$ map an input/output invariant to each location
- ▶ $\rho_{\mathcal{A}} \subseteq Q_{\mathcal{A}} \times \Xi[\mathcal{X}_{\mathcal{A}}] \times Acts_{\mathcal{A}} \times 2^{\mathcal{X}_{\mathcal{A}}} \times Q_{\mathcal{A}}$ transition relation

Solving Timed Games

Checking for winning strategy of I

- ▶ compose automaton with Tick automaton
- ▶ check if there is strategy for $\square\diamond q_1 \vee \diamond\square bl = O$
- ▶ use algorithm for untimed games
- ▶ similar for player Output



Solving Timed Games

Reachable states

- ▶ definable by clock conditions
- ▶ use algorithms for timed automata [AD94]

Well-formedness

Check: *reachable* \implies *both players have winning strategy*

Summary




- ▶ new approach: model interface as asymmetric game
- ▶ restrict moves of input to guarantee safety and liveness
- ▶ optimistic
- ▶ automata representation allows use of existing algorithms

⇒ better model for interaction of real-time components

Thank you for your attention!

Questions?

References

-  Rajeev Alur and David L. Dill.
A theory of timed automata.
Theoretical Computer Science, 126(2):183–235, 1994.
-  L. de Alfaro, T. Henzinger, and M. Stoelinga.
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-  Oded Maler, Amir Pnueli, and Joseph Sifakis.
On the synthesis of discrete controllers for timed systems
(extended abstract), 1995.